INSTRUCTION MANUAL FT-290R



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FT-290R MODEL CHART

N	10DEL	FREQUENCY COVERAGE	PRESET FREQUENCY	FREQUENCY STEPS (FM)	FREQUENCY STEPS (SSB/CW)	REPEATER SHIFT (FM)	TONE BURST FREQUENCY (OPTIONAL)	TONE ENCODER (OPTIONAL)	TONE SQUELCH (OPTIONAL)
	(A)	144 – 147.999 ^{MHz}	147.000MHz	10kHz (5kHz)	100Hz (1kHz)	±600kHz	1800Hz	FTE-36	FTS-32R
	(B)	144 – 145.999 ^{MHz}	145.000MHz	25kHz (12.5kHz)	100Hz (1kHz)	±600kHz	1750Hz	FTE-36	FTS-32R
	(C)	144 – 147.999 ^{MHz}	145.000MHz	25kHz (12.5kHz)	100Hz (1kHz)	±600kHz	1750Hz	FTE-36	FTS-32R
	(D)	144 – 147.999 ^{MHz}	145.000MHz	10kHz (5kHz)	100Hz (1kHz)	±600kHz	1750Hz	FTE-36	FTS-32R
	(E)	144 – 147.999 ^{MHz}	147.000MHz	10kHz (5kHz)	100Hz (1kHz)	±600kHz	1750Hz	FTE-36	FTS-32R

Note: Model F is for use in Japan only, and cannot be readily converted to other models.

FT-290R 2 METER PORTABLE TRANSCEIVER



INTRODUCTION

The FT-290R is a highly sophisticated, compact multi-mode transceiver for the two meter amateur band. Featuring PLL synthesis in 100 Hz, 1 kHz, 5 kHz, or 10 kHz steps, the FT-290R utilizes a Liquid Crystal Display for digital readout of the operating frequency. Ten memories, scanning of the band or memory channels, two VFOs, and receiver offset tuning make the FT-290R a significant breakthrough in technology.

Powered by eight "C" size dry cells or Ni-Cd batteries (not supplied), the FT-290R is completely self-contained and portable. A telescoping whip antenna is built into the FT-290R, for convenient portable operation. And a high-performance noise blanker is also included, for minimizing interference caused by impulse noise.

Power output is 2.5 watts, switchable to 0.5 watt for battery conservation. For memory backup purposes, a lithium cell is included, providing an estimated lifetime of five years because of the extremely low current consumption of the memory circuitry. The light weight, portability, and efficiency of the FT-290R make it suitable for field satellite operation, emergency FM work, or vacation enjoyment.

We recommend that you read this manual in its entirety, so as to understand more completely the many features of the exciting new FT-290R. With proper care in operation, this equipment will provide many years of reliable performance.

SPECIFICATIONS

GENERAL

Frequency coverage:

144 - 148 MHz; 144 - 146 MHz

(as per your local regulations)

Modes of operation:

SSB (USB, LSB), CW and FM

Synthesizer steps:

SSB/CW: 100 Hz, 1 kHz

FM: 5 kHz, 10 kHz (12.5 kHz, 25 kHz,

depending on local requirements)

Power requirements:

8 C - size dry battery cells or

8 C - size Ni-Cd battery cells

External: 8.5 - 15.2 V DC

Memory backup: built-in lithium bat-

tery cell

Current consumption:

60mA on receive;

800mA on transmit (2.5W RF, FM)

Antenna impedance:

50 ohms

Case size:

58(H) x 150(W) x 195(D) mm

Weight:

1.3kg. without batteries

TRANSMITTER

Power output:

2.5 watts at 12 volts

Carrier suppression:

Better than 40 dB

Spurious radiation:

Better than 60 dB

Unwanted sideband suppression:

Better than 40 dB

Tone burst frequency:

1800 Hz (U.S.A. model)

1750 Hz (other models)

Frequency response:

300 - 2700 Hz (-6 dB)

FM deviation:

±5 kHz

Microphone impedance:

600 ohms

RECEIVER

Circuit type:

SSB/CW: Single conversion

superheterodyne

FM:

Double conversion

superheterodyne

Intermediate frequencies:

1st IF 10.81 MHz

2nd IF 455 kHz (FM)

Sensitivity:

SSB/CW: $0.5\mu V$ for 20 dB S/N

FM: $0.25\mu V$ for 12 dB SINAD

Selectivity:

SSB/CW: 2.4 kHz at 6 dB down;

4.1 kHz at 60 dB down

FM:

14 kHz at 6 dB down;

25 kHz at 60 dB down

Image reduction:

Better than -60 dB

Audio output impedance:

8 ohms

Audio output:

1 watt @10% THD

Specifications subject to change without notice or obligation.

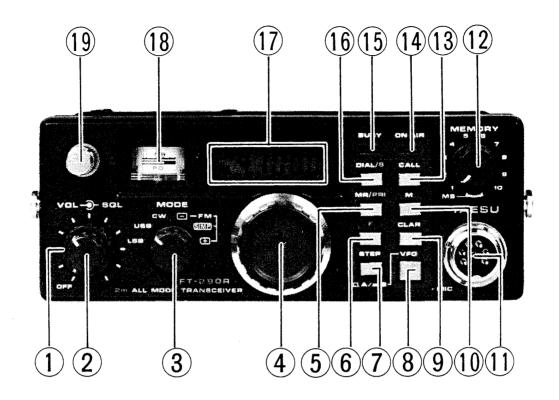
SEMICONDUCTORS

ICs:		Transistors:		Diodes:	
HD44820A62	1	2SA733P	2	1S188FM (Ge)	12
ICL7660CPA	1	2SA733Q	1	1SS53(Si)	49
MC1496P	1	2SC496Y	1	10D1(Si)	2
MC3357P	1	2SC535A	5	MI301(Si)	2
MC14001B	1	2SC945P	4	V05B (Si)	1
MC14069UB	2	2SC1583	1	1 SS 97	1
TC5082P	1	2SC1947	1	(Schottky Barrier))
TP0401	1	2SC2026	1	1SV50 (Varactor)	1
μ PC575-C2	1	2SC2053	1	1SV68 (")	1
μ PC577H	1	2SC2603E	16	1SV69 (")	8
μPD2819-C	1	2SC2786L	2	1T25 (")	1
		MPS-A13	1	HZ6C-1L (Zener)	1
FETs:				RD5.6EB-3(")	2
2SK30A-Y	1			RD6.8EB-3(")	1
2SK168D	2			TLG205(LED)	1
2SK192GR	4			TLR205(LED)	1
2SK193F	1		•		
3SK51-03	1			LCD:	
3SK59GR	1			H1313A	1
3SK59Y	1				
3SK73Y	4				

ACCESSORIES

MICROPHONE YM-47 (M3090033)	1
MICROPHONE HANGER (R0071360)	1
SHOULDER STRAP (R7070600B)	1
EXTERNAL POWER SUPPLY PLUG P-200 (P1090139)	1
EXTERNAL SPEAKER PLUG C-107 (P0090034)	1

FRONT PANEL CONTROLS AND SWITCHES



(1) SQL

The squelch control silences the receiver in the FM mode when no stations are being received on the channel in use. The SQL control should only be advanced to the threshold point of background noise silencing; further advancement of this control will lead to reduced sensitivity to weak signals.

(2) **VOL**

This is the audio gain control for the receiver, as well as the main ON/OFF switch for the transceiver.

(3) MODE

This switch selects the desired mode: LSB, USB, CW, or FM.

(4) MAIN DIAL

The main tuning dial is used for selection of operating frequencies using the two main VFOs or the clarifier. In the LSB, USB, and CW modes, synthesizer steps of 100 Hz or 1 kHz are programmed, while on FM the channel steps are 5 kHz or 10 kHz each. In the clarifier mode, the synthesizer moves in 100 Hz steps.

(5) MR/PRI

This switch selects either the memory recall mode or priority channel operation. If only the MR/PRI switch is pressed, the memory channel selected by the MEMORY rotary switch will be activated. If the yellow F button is first pressed, then the MR/PRI button, priority channel operation will be selected.

(6) F

1.20

The yellow "F" (Function) button activates either the priority channel mode or the memory split mode. The F button itself does not select a mode, but it programs the microprocessor to select the mode labeled in yellow letters in either of the two switches immediately above the F button: DIAL/S or MR/PRI.

(7) **STEP**

This switch selects the desired synthesizer steps. In the LSB, USB, or CW mode, the preset mode is 1 kHz per step. Press the STEP button to switch to 100 Hz steps. A second press of this switch returns you to 1 kHz steps. In the FM mode, the preset is for 10 kHz steps. Pressing the STEP switch selects 5 kHz steps, while a second press returns you to 10 kHz steps.

(8) VFO Switch

The VFO button selects one of the two internal VFOs on the FT-290R. Upon switch-on, VFO-A is automatically selected. Press the VFO switch to select VFO-B, and dial up the new frequency. A second press of the button releases the switch, returning you to VFO-A.

(9) CLAR

This switch activates the receiver offset tuning feature (Clarifier). The clarifier allows ± 10 kHz of offset from the transmit frequency, tuned in 100 Hz steps (all modes).

(10) M

The M (Memory) button is used to store a frequency in memory.

(11) MIC

This seven pin jack accepts microphone audio input, the scanning control lines, and the PTT (Push to Talk) control line. Microphone impedance is 500 ohms.

(12) MEMORY

The memory channel selector is used to choose any of the 10 memory channels. In the MS (Memory Scan) position, scanning of the memories may be performed.

(13) CALL

When this button is pressed (FM mode only), a 1800 Hz (or 1750 Hz) tone will be superimposed on the microphone line, and the PTT switch line will be grounded, activating the transmitter. This allows manual-length access of repeaters requiring a burst tone.

(14) ON AIR

This indicator lights up while transmitting.

(15) BUSY

This indicator lights up when the main squelch is opened up by an incoming signal.

(16) DIAL/S

When the DIAL/S button alone is pushed, tuning is accomplished by the main dial on either VFO-A or VFO-B. If the F button is pushed, then the DIAL/S button, the memory split mode will be selected, for receiving on the memory while transmitting on the VFO.

(17) DIGITAL DISPLAY

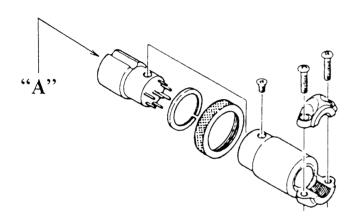
The digital display uses a liquid crystal display for indication of the operating frequency and mode. The frequency readout displays the last five digits of the operating frequency, with resolution to 0.1 kHz. Indicators are also provided for indication of clarifier operation ("CLAR"), memory channel operation ("M"), or memory split operation ("—" on transmit).

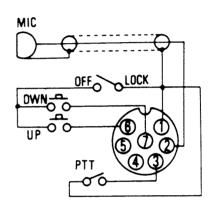
(18) S/PO

The meter allows determination of incoming signal strength and relative power output. The meter is also used for checking battery operation.

(19) WHIP ANTENNA

The built-in whip antenna is satisfactory for most portable operation. When using an external antenna, the whip should be telescoped fully into the transceiver. Conversely, when an external antenna is not used, the whip should always be fully extended.

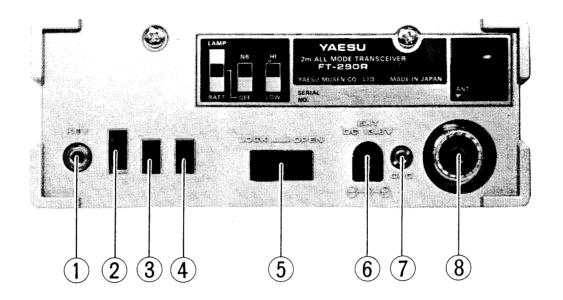




Viewed from "A" Side

YM-47 MICROPHONE PLUG CONNECTIONS

REAR APRON SWITCHES AND JACKS



(1) **KEY**

This jack is used for the keying input line. Use a miniature phone plug for connection to your telegraph key or keyer. The key-up voltage is $7\,\mathrm{V}$, and the key-down current is $0.3\,\mathrm{mA}$.

(2) LAMP/BATT CHECK

With this switch in the LAMP mode, the front panel meter and LCD display will become illuminated for nighttime operation. If the power switch (on the VOL control) is off, this lamp will not come on, thus preventing inadvertent battery discharge.

In the BATT mode, the battery voltage is checked. The meter needle should deflect at least to the dividing line between the green and white zones of the meter scale. If not, the batteries will require replacement or recharging.

(3) NB

This switch activates the built-in noise blanker. While no blanker can be expected to eliminate all types of noise, such as white noise, etc., this blanker should prove highly effective in minimizing pulse-type noise such as that caused by automotive ignition systems.

(4) HI/LOW

This switch selects power outputs of 2.5 watts (HI) or 0.5 watt (LOW).

(5) CASE LATCH

This mechanism provides easy opening and closing of the cabinet for battery removal.

(6) EXT DC 13.8V

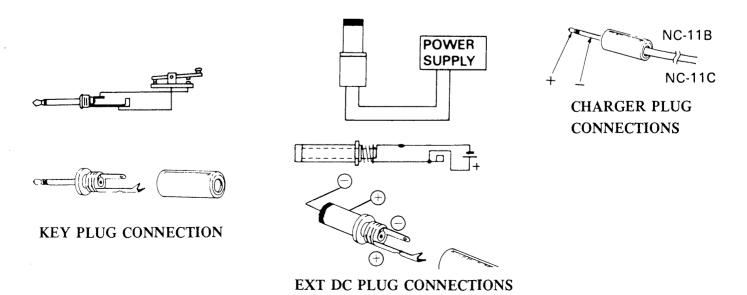
Use this jack for connection to an external DC supply. Never exceed 15 volts at this jack, and never apply AC power of any kind at this point. Also, be absolutely certain that DC power of the proper polarity is applied; when replacing DC plugs, check to be sure that the plug is wired correctly, as there is little standardization in the world for the power plug used for the FT-290R. Failure to observe these simple precautions will void any and all warranties on this equipment.

(7) CHG

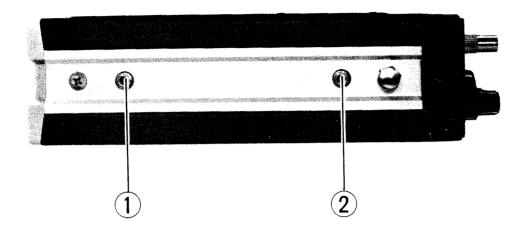
The external charge jack accepts charging voltage from the NC-11B/C battery charger (Option). When using alkaline or other dry cell batteries, do not attempt to recharge them. Use only C size Ni-Cd cells (available from your Yaesu dealer) if you desire rechargeable cells.

(8) ANT

This is a UHF type connector for use with an external antenna of 50 ohms impedance (nominal). When using an external antenna, the internal whip should be telescoped fully inside the radio.



SIDE PANEL JACKS

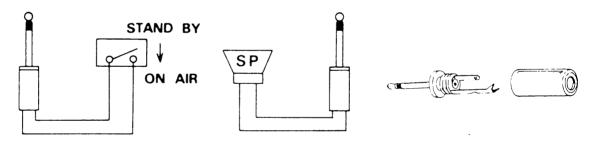


(1) STAND BY

This jack is wired in parallel with the PTT line of the microphone, allowing the use of a footswitch to activate the transmitter.

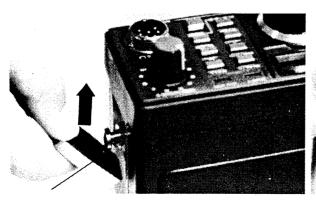
(2) EXT SP

Use this jack to connect an external speaker. The output impedance is 8 ohms.



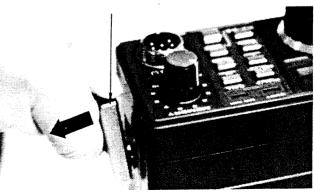
SHOULDER STRAP ATTACHMENT AND REMOVAL

ATTACHMENT



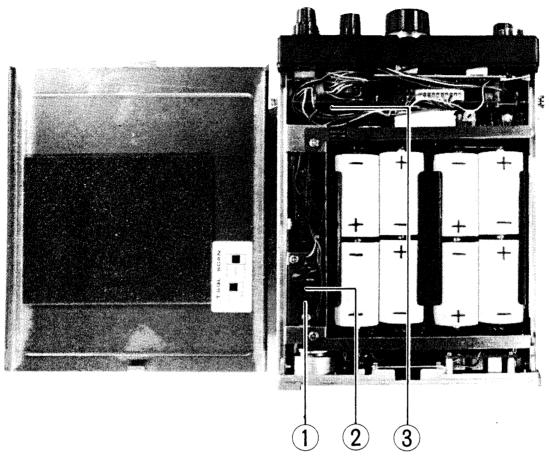
Press pin into hole, then pull up tab.

REMOVAL



Press with thumb while lifting tab, swiveling back and forth slightly until pin disengages.

INTERNAL SWITCHES



(1) T SQL

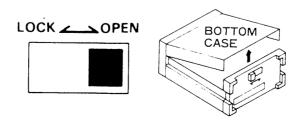
When the optional tone squelch unit is installed, this switch will place the unit in operation.

(2) SCAN

This switch selects scanning stop on a busy or clear channel, per your requirements. Manual scanning can also be selected, if desired.

(3) BACKUP

This switch activates the memory backup feature. Once the batteries are correctly installed, this switch may be turned on and left on indefinitely. See the operation section for details.



ANTENNA CONSIDERATIONS

The FT-290R is designed for use into a 50 ohm resistive load. While departures from this value are of no significant consequence, it is possible to damage the transmitter circuitry if no antenna is connected and the transmitter is activated.

For most portable use, the built-in telescoping whip antenna will provide satisfactory operation. For base station use, any of the popular beam or phased arrays will provide excellent performance, so long as they present the proper impedance to the transmitter and have been optimized for best forward gain.

When an external antenna is being used, the whip antenna should be telescoped fully into the FT-290R. Conversely, when no external antenna is connected, the whip should be fully extended. Failure to observe these simple precautions will void all warranties on this unit.

BATTERY INFORMATION

The FT-290R is designed for use with eight size C Ni-Cd rechargeable cells or eight dry cells of the same size. When using alkaline cells or other dry cell types, no "dummy" battery is required, as the FT-290R will tolerate the slightly elevated voltage of these batteries as compared to Ni-Cd cells.

The install batteries, set the rear panel lever to OPEN to unlock the case. The bottom cover may then be carefully removed, exposing the battery holder. Install the eight new cells, being absolutely certain to observe the proper polarity.

WARNING

Serious damage can occur if incorrect battery polarity is used. Our warranty does not cover damage caused by incorrect polarity in the battery compartment.

If Ni-Cd cells are used, the optional NC-11B/C battery charger may be used to return the cells to a full charge. Allow the cells to discharge minimum operating voltage before recharging them. If the cells are only partially exhausted, and repeatedly recharged in this condition, they may develop a memory for this level, and not provide full discharge capability.

Ni-Cd cells suitable for use in the FT-290R are available from your Yaesu dealer. Ask also for the MMB-11 Mobile Mounting Bracket, FL-2010 Linear Amplifier, YM-49 and YM-50 microphones, and CSC-1A vinyl carrying case for the FT-290R.



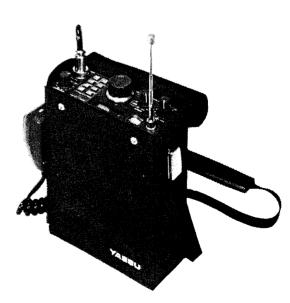
NC-11B (117V AC)



NC-11C (220-234V)



FT-290R/FL-2010/MMB-11/YM-47



FT-290R/CSC-1A/YM-47

OPERATION

The tuning procedure for this transceiver is not complicated. However, because microcomputer circuitry is used extensively throughout the transceiver, this section should be read thoroughly, so as to understand all of the features that are made available. Note that off-frequency operation could occur without proper setting of the controls, because of the many options the operator has for frequency selection.

INITIAL CHECK

Before operating the transceiver, be certain that the necessary batteries are installed in the case, as described previously. Extend fully the built-in telescoping whip antenna, if used. If an external antenna is used, be certain that the internal whip antenna is fully nested into the FT-290R. If an external voltage source is used instead of batteries, confirm that the proper DC voltage is being applied to the rear panel jack, and that the proper polarity is used.

FREQUENCY READOUT

Frequency display is provided by a five-digit Liquid Crystal Display (LCD) system. Resolution of the last five digits of the operating frequency is provided to 0.1 kHz.

When operating on a memory channel, the letter "M" will appear on the left side of the display. The memory channel number will not be shown, as it is already shown on the selector switch labeling. The actual memorized frequency will be displayed, however.

SSB OPERATION

Preset the controls and switches as follows:

OFF (Fully counterclockwise) VOL Fully counterclockwise **SQL** Desired mode, USB or LSB **MODE**

Channel 1 **MEMORY**

OFF LAMP (Rear apron) NB **OFF** HI/LOW HI

Rotate the VOL switch out of the click-stop, and adjust the volume level for a comfortable audio output from the speaker. The LCD display will indicate the operating frequency. Initially (first switch-on after the memory backup battery has been installed), the display will indicate 147.000 MHz as a preset frequency; thereafter, when the transceiver is switched on, the backup feature will keep you locked onto the frequency and mode (dial or memory) last used when you switched the unit off.

The STEP switch is used to select the desired synthesizer step, 1 kHz or 100 Hz per step (SSB/CW modes). If you rotate the main tuning dial, initially the synthesizer will provide 1 kHz steps. Press the STEP button once, and you will note that the steps are now 100 Hz (0.1 kHz) each. Another pressing of the STEP button will return the selection to 1 kHz/step.

While most operation on 2 meters is on USB, there are many times (especially during satellite operation) when LSB operation is needed. Simply rotate the mode switch to LSB to select that mode. There is no passband tuning adjustment needed on this transceiver.

Rotate the main tuning dial until an SSB signal is heard. Using the 100 Hz/ step mode, tune in the signal until a natural reproduction of the voice signal is obtained.

To transmit, close the microphone PTT switch, and speak at a normal level into the microphone. Release the PTT switch for receiver recovery. The microphone amplifier gain is preset in this transceiver and requires no further adjustment for normal operation.

If the station you are in contact with begins to drift, you may follow the station by activating the receiver offset tuning control (CLARIFIER). Push the CLAR button, and then rotate the main tuning dial (or push the scanning controls) until the desired frequency is reached. In the CLAR mode, the synthesizer automatically is set to the 100 Hz/step mode, and the STEP button is disabled. The clarifier leaves the transmit frequency unchanged.

Push the CLAR button again to return to normal operation with the clarifier off. If you switch the clarifier on again, the receiver will not return to the last offset frequency, but rather will initiate on the current operating frequency.

For satellite operation, it is possible for the transmit frequency to be varied while transmitting.

If pulse-type noise is encountered, the rear apron NB (Noise Blanker) switch may be activated. While no noise blanker can be expected to eliminate all types of atmospheric and man-made noise encountered in day-to-day operation, the FT-290R noise blanker should be quite helpful in reducing interference caused by pulse noise such as that produced by automobile ignition systems.

To reduce power for local communication, place the HI/LOW power switch in the LOW position. In this position, the PEP output power is approximately 500 mW. Battery consumption will be greatly reduced by using the low power position whenever possible.

FM OPERATION

Preset the controls and switches as described for SSB operation, but set the MODE switch to FM/SIMP.

In the FM mode, the synthesizer steps provided are 5 kHz and 10 kHz per step (the clarifier steps are still 100 Hz/step). When you are changing modes from SSB to FM, and were last operating on other than a 5 kHz or 10 kHz step, the microprocessor will automatically move you to the next higher or lower 5 kHz or 10 kHz step upon the first click of the main tuning dial (or first stepping of the scanner).

Rotate the main tuning dial (or operate the scanning controls) until the desired frequency is reached. To transmit, close the PTT switch, and speak into the microphone in a normal voice. Release the PTT switch for receiver recovery.

For repeater operation, selection of the standard ± 600 kHz splits is provided on the front panel. For -600 kHz shift, set the MODE switch to FM/-, and for ± 600 kHz shift, select FM/+. This selection can be made either during main dial or memory operation.

For operation on odd splits, use a combination of the memory system and the main tuning dial. First, store the desired receive frequency in any memory channel. Now use the main dial to select the desired transmit frequency. Next push the yellow F and S buttons. You will now be receiving on the memory channel just programmed. When you close the PTT switch, you will be transmitting on the main dial frequency. If you desire to listen on several memory channels, the memory channel selector may be rotated as desired.

The front panel CALL switch activates a manual-length 1800 Hz tone for repeater access. When this button is pushed, the transmitter is activated and the access tone is superimposed on the transmit signal.

Rotate the SQL (Squelch) control fully counterclockwise. Now turn the VOL control out of the click-stop to turn the transceiver on. Advance the volume control for a comfortable listening level.

When the channel is clear, adjust the SQL control so the background noise just disappears. This threshold point is the point of maximum sensitivity, and the squelch control should not be advanced beyond this point too far, or the squelch will not respond to weak signals.

CW OPERATION

- (1) The synthesizer steps selected in the CW mode are identical to those used for SSB operation.
- (2) Connect a key to the rear panel KEY jack, using a miniature phone plug. The key-up voltage is 7V, while the key-down current is 0.3mA, so most electronic keyers that close completely to ground will work well with the FT-290R.
- (3) Set the MODE switch to CW.
- (4) Close the PTT switch on the microphone to switch to the transmit mode. If desired, a footswitch may be used with the FT-290R. The STAND BY jack, located on the side of the transceiver, is wired in parallel with the PTT line on the microphone. This may be used in situations where the microphone is not the most efficient means of activating the transmitter.
- (5) The clarifier may be used for following unstable signals. The clarifier allows offset tuning in 100 Hz steps away from the transmit frequency. See the section on clarifier operation for details.

CLARIFIER OPERATION

Offset tuning is provided on receive, for tracking of unstable or Doppler-shifted signals. The clarifier may be used either on VFO frequencies or memory frequencies.

To activate the clarifier, push the CLAR button once. The letters "CLAR" will appear on the digital display. Now, tune the receiver as needed to follow the unstable signal. The synthesizer automatically programs 100 Hz steps for clarifier operation. A frequency shift of up to 10 kHz can be accomplished by using the clarifier.

When you close the PTT switch, the digital display will revert to the frequency programmed **before** the clarifier was switched on. In other words, your transmit frequency has remained unchanged, while your receive frequency has been varied.

A second press of the CLAR button will cancel clarifier operation. If the CLAR button is then pressed again, switching the clarifier back on, the clarifier is zeroed to the original operating frequency (before any offset), not to the offset frequency tuned previously.

THE UP/DWN CONTROLS ON THE MICROPHONE MAY BE USED FOR SCANNING DURING CLARIFIER OPERATION.

VFO SELECTION

Two VFOs are available on the FT-290R for split frequency operation. The VFO selector button is the largest of the eight mode selector buttons on the front panel of the FT-290R. This switch is a push-push type, not the momentary type used for the other mode selector buttons.

For VFO B operation, push the VFO button once; the switch will hold inward, and the desired frequency may then be dialed up. Be certain, of course, that you are in the DIAL mode. To return to VFO A, simply push the VFO button again to release the switch.

It is not possible to receive on one VFO while transmitting on another. For frequency splits of 10 kHz or less, use the clarifier to achieve this function. Otherwise, use the MEMORY SPLIT mode described elsewhere in this manual.

NOTE REGARDING BACKUP OPERATION

When a backup battery or main batteries are first installed in the FT-290R (after service or replacement), it is necessary to reset the microcomputer properly. Failure to follow a simple sequence of steps may cause erratic operation.

- (1) Set both the VOL and memory backup switches (memory backup switch is located inside the cabinet, as shown on page 11) to OFF.
- (2) Replace the memory backup battery and main batteries (if removed).
- (3) Turn the VOL control out of the click-stop, turning the transceiver ON.
- (4) Now turn the backup switch to ON. The CPU is now reset, and the backup switch may be left on indefinitely, owing to the very low current drain in the backup mode.

MEMORY OPERATION

Ten memory channels are available for storage and recall of favorite operating frequencies. The procedure for entry and recall of memory channels is extremely simple.

Push the DIAL switch for normal tuning, using the main tuning dial. When you have found a frequency you wish to store in memory (for example 146.520 MHz), rotate the MEMORY switch to 1 (channel 1) and push the M (memory store) button. If you wish to store 146.490 MHz in channel 2, rotate the main dial to that frequency, rotate the MEMORY switch to channel 2, and push M, and so forth. This procedure may be repeated for all 10 memory channels.

To recall these frequencies, push the MR button (memory recall) and rotate the MEMORY switch to select the desired channel. One push of the M button will keep you on memory recall operation until the DIAL button is pushed again to return you to main dial tuning. Note that there is no formal erasure procedure for memory channels. When you push the M button, the previous frequency stored in that position will be erased. Until a frequency is programmed into a memory channel (from initial switch on of the transceiver), 147.000 MHz will be preset in all memory channels.

SCANNER OPERATION

The UP/DOWN scanning controls on the microphone may be used to control the operating frequency.

When in the DIAL mode, one push of the UP button will cause the frequency to advance upward by one step of the synthesizer (the step size being programmed by the mode switch and the STEP button). If you hold the UP button down for more than 1/2 second, the scanner will become engaged, and you will begin scanning up the band. Push the UP or DN button or the PTT switch to halt the scan.

Scanning toward a lower frequency is achieved by using the same procedure, only using the DN button on the microphone.

To scan only the memory channels, rotate the MEMORY selector to either of the MS (Memory Scan) positions, and press the MR button. Now, when you push and hold the UP or DN button, the scanner will search the memory channels only. Manual halting of the scan is accomplished by pushing the UP, DN, or PTT switches as before.

Inside the case of the radio, the BUSY-MAN-CLEAR switch allows selection of one of three scan halt modes. In the MAN (Manual) position, scanning is halted as discussed above. If the BUSY position is selected (see Page 11), the scanner will search until a busy channel (one occupied by a station strong enough to break the main squelch) is received. The scan will then pause on that frequency for five seconds. If you choose to stay on that frequency, press one of the scan control buttons or the PTT switch. While in the PAUSE mode, the decimal point farthest to the right will blink; when you push a button to halt the resumption of the scan, the blinking will stop.

To scan for a clear channel (one where the squelch does not open), set the BUSY-MAN-CLEAR switch to CLEAR. The scan will halt, and the decimal point will blink, as in the previous section. Press the UP, DN, or PTT switch to cancel the pause/resume feature and hold on the frequency you stopped at.

Memory scan halting follows the same format as main dial scanning.

PRIORITY CHANNEL OPERATION

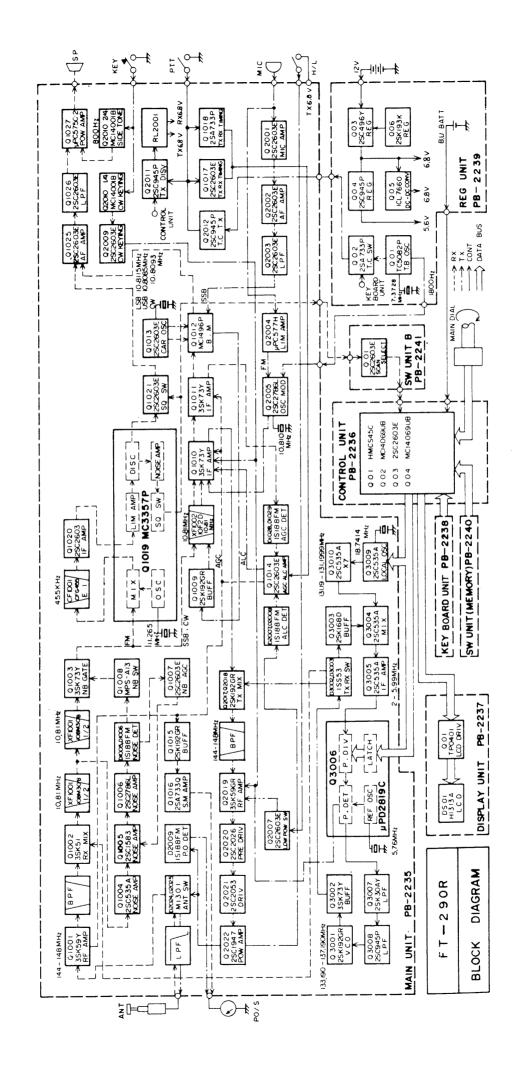
Priority channel operation uses a combination of the main dial VFO and the memory. It can be used in conjunction to the automatic scan stop feature of the microprocessor, if desired. The steps for priority channel operation are detailed below.

- (1) Program into memory the desired priority channel. Do not recall the channel at this time.
- (2) Dial up a basic operating frequency on the main VFO. This will be your main operation channel during priority channel operation.
- (3) Set the BUSY-MAN-CLEAR switch to BUSY or CLEAR, as desired.
- (4) Now push the yellow F button, followed immediately by a press of the MR/PRI button. The letter "P" will appear on the digital display, signifying priority channel operation. The display will then show the VFO frequency, with a flash every five seconds to the memory channel being checked for activity. When the memory channel is busy or clear (depending on your instructions), the scanner will halt on the memory channel. The pause/restart feature does not function in this mode; to restart, simply press the F and MR/PRI buttons again.
- (5) If the scan stop switch is set to the MAN position, the CPU will have no instructions for halting the scan. Simply press the DIAL or MR button to select the desired channel under this mode of operation. If you hit the PTT switch during manual priority channel operation, the checking of the priority channel will be delayed by five seconds.

MEMORY SPLIT OPERATION

The memory split operation mode is useful for covering unusual repeater splits or other occasions where the receive frequency may be fixed, but the transmit frequency is variable. In this mode, you receive on a memory channel, while transmitting on the VFO.

- (1) Store the desired receive frequency into a memory channel.
- (2) Dial up the desired transmit frequency on the main dial.
- (3) Now press the yellow F and DIAL/S buttons. You will be receiving on the memory, while transmitting on the VFO.
- (4) For transmitting purposes, either VFO A or VFO B may be used. Set the VFO selector as needed.



CIRCUIT DESCRIPTION

The block diagram and circuit description to follow will provide you with a better understanding of this transceiver. Please refer to the block and schematic diagrams for specific circuit details.

RECEIVER

The RF signal from the antenna jack is applied to a lowpass filter and diode antenna switch, consisting of D_{2024} and D_{2025} (MI301) and is then fed to an RF amplifier, Q_{1001} (3SK59Y), where the signal is amplified with excellent rejection of cross modulation and intermodulation. The amplified signal is then fed through three sections of Auto-tuning filter to reject unwanted signals which may cause intermodulation at the 1st Mixer, Q_{1002} (3SK51-03). Here, the signal is mixed with a local signal delivered from the local oscillator buffer, resulting in a 10.81 MHz first IF signal.

The first IF signal passes through a pair of monolithic crystal filters, XF_{1001} (108M30B), which have bandwidths of ±15 kHz. It is then amplified by Q_{1003} (3SK73Y), which acts as a switch, driven by the NB (noise blanker) circuit. The amplified signal from Q_{1003} is fed to IF amplifiers for FM or SSB/CW.

A portion of the RF signal from monolithic crystal filter XF_{1001} is fed to a noise blanker amplifier circuit, consisting of Q_{1004} (2SC535A), Q_{1005} (2SC1583) and Q_{1006} (2SC2786L), where the signal is amplified to a level sufficient to drive the noise blanker rectifier and noise blanker AGC circuits.

When the carrier of a noise-free modulated signal is received, the signal at the noise amplifier is rectified by D_{1005} and D_{1006} (1S188FM), producing a DC voltage. The DC voltage is amplified by Q_{1007} (2SC2603E), which charges C_{1042} for AGC purposes. The AGC voltage is used to control the gain of Q_{1005} and Q_{1006} .

When a pulse-type noise is received, D_{1005} and D_{1006} rectify the noise, and it is then fed through D_{1042} (1S188FM) to a DC amplifier, Q_{1008} (MPSA13), which drives gate 2 of Q_{1003} .

The FM mode signal is fed to the mixer section of Q_{1019} (MC3357P), where the signal is mixed with a 11.265 MHz local signal, oscillated by its local oscillator section, and fed through a ceramic filter, CF_{1001} , which has a $\pm 7.5 \, \text{kHz}$ bandwidth. It is then amplified by IF amplifier Q_{1020} (2SC2603E), and fed back to the IF amplifier/limiter section of Q_{1019} , where the IF signal is amplified and any amplitude modulation in the signal is rejected. Next, the signal is delivered to the discriminator section, which produces an audio output in response to a corresponding shift in the 455 kHz IF signal.

When no carrier is present in the 455 kHz IF, the high frequency noise at the discriminator output, which passes through a bandpass filter, is amplified by the noise amplifier in Q_{1019} and detected by D_{1032} (1S188FM), producing a DC voltage. This voltage activates a switch in Q_{1019} which grounds the base of Q_{1021} (2SC2603E), to turn off the AF output from the discriminator to the AF amplifier.

When a carrier is present in the 455 kHz IF, the noise is removed from the discriminator and the audio amplifier then returns to normal operation. The squelch threshold sensitivity is set by VR_{1b} .

SSB and CW mode IF signals from Q_{1003} are passed through a crystal filter, XF_{1002} , which has a very high shape factor, to reduce signals on adjacent frequencies. The filtered SSB signal is amplified by Q_{1010} and Q_{1011} (3SK73Y), and then fed to the balanced demodulator, Q_{1012} (MC1496P), where a carrier signal is applied from carrier oscillator Q_{1013} (2SC2603E), resulting in an AF signal, which is then fed to the AF amplifier.

The AF amplifier consists of Q_{1025} , Q_{1026} (2SC2603E) and Q_{1027} (μ PC575C-2). The AF signal from the FM discriminator and the balanced demodulator are amplified by Q_{1025} , and fed to active lowpass filter Q_{1026} , where the AF signal above 3 kHz is cut off. The AF signal is then delivered to AF power amplifier Q_{1027} , providing approximately 1 watt of audio output to the speaker.

S-METER AND AGC CIRCUITS

A portion of the IF signal from Q_{1011} is rectified by D_{1028} and D_{1029} (1S188FM) and amplified by Q_{1014} (2SC2603E). This amplified DC voltage controls gate 2 of MOS FET's in the IF amplifier. A portion of the AGC signal is buffered by Q_{1015} (2SK192GR), and fed to the S-meter amplifier, Q_{1016} (2SA733Q), providing a DC voltage for the S-meter deflection.

TRANSMITTER

The discussion of the signal flow on transmit will be made on a mode-by-mode basis.

SSB

The audio input signal from the microphone is amplified by Q_{2001} and Q_{2002} (2SC2603E), and then delivered to an active lowpass filter, Q_{2003} (2SC2603E), where the unwanted frequency spectrum above 3 kHz is cut off. This amplified speech signal is fed to balanced modulator Q_{1012} (MC1496P), where the audio signal modulates the 10.81 MHz carrier signal delivered from the carrier oscillator, Q_{1013} (2SC2603E), resulting in a 10.81 MHz double-sideband signal. The DSB signal is amplified by a buffer, Q_{1009} (2SK192GR), and delivered to crystal filter XF₁₀₀₂ (10F2D), where the unwanted sideband is sliced out, resulting in a single sideband signal. This SSB signal is then amplified by Q_{1010} (3SK73Y), and delivered to a mixer, Q_{2017} and Q_{2018} (2SK192GR), where the SSB signal is mixed with a local signal from the PLL local oscillator buffer, Q_{3002} (3SK73Y), resulting in a 144 \leftarrow 148 MHz SSB signal.

The SSB signal passes through an auto-tuning filter consisting of T_{1002} – T_{1005} and varactor diode D_{1018} – D_{1021} , where the resonant frequency is tuned exactly to the transmitting frequency, thus minimizing spurious radiation. The signal is then amplified by four stages of straight amplifier consisting of Q_{2019} (3SK59GR), Q_{2020} (2SC2026), Q_{2021} (2SC2053) and Q_{2022} (2SC1947), providing a power output of 2.5 watts over the range of 144-148 MHz.

Finally, this signal passes through an RF diode switch and lowpass filter to the ANT connector and built-in telescoping antenna.

FM

The speech signal from the microphone is amplified and limited in amplitude by Q_{2004} ($\mu PC577H$). It is then fed through a lowpass filter to eliminate harmonics above the speech range, caused by clipping. Next it goes to a frequency modulator consisting of Q_{2005} (2SC2786L) and D_{1005} (1SV68), where the 10.81 MHz oscillating frequency is modulated, corresponding to the AF signal from Q_{2004} . Thus, an FM signal of 10.81 MHz is produced. This signal is then delivered to the IF amplifier, Q_{1010} , and the signal path then becomes identical to that of the SSB signal.

CW

For CW, the 10.8093 MHz carrier is generated by Q_{1013} (2SC2603E), and delivered to the balanced modulator Q_{1012} . The key line is connected to keying control IC (quad NOR gate) Q_{2010} (MC14001B), which drives keying switch Q_{2009} (2SC2603E) to control the DC bias voltage sent to the source of Q_{1010} and Q_{2019} . As a result, the RF signal is turned on and off.

From this point, the signal path is identical to that of the SSB signal.

The control signal from Q_{2010} is also fed to the sidetone oscillator consisting of two sections of gate circuits in Q_{2010} , which oscillate sidetones of about 800 Hz and the sidetone signal is then delivered to the AF amplifier.

Tone Burst Circuit

When the T. CALL switch is pressed, the base of Q_{4002} (2SA733P) is grounded and a DC voltage is applied to tone burst oscillator Q_{4001} (TC5082P) to generate a 1800 (1750) Hz tone signal. The tone is superimposed on the transmit signal as long as the switch is held.

PLL Circuit

The PLL circuit is composed of a reference crystal oscillator, programmable divider, VCO (voltage controlled oscillator), PLL local mixer, PLL local oscillator, lowpass filter and phase comparator. The PLL produces the local signal for the receiver and transmitter stages, using a synthesis scheme which utilizes 100 Hz steps throughout the range.

The VCO oscillator, Q_{3001} (2SK192GR), generates a signal at 133.190 – 137.190 MHz. The oscillator frequency is controlled by varactor diode D_{3001} (1T25), which varies the capacitance of the oscillator tuned circuit in accordance with the control voltage supplied from an active lowpass filter consisting of Q_{3007} (2SK30AY), and Q_{3008} (2SC945P).

The output signal from Q_{3001} is amplified by buffer Q_{3002} (3SK73Y) and delivered to TX mixer Q_{2017}/Q_{2018} and RX mixer Q_{1002} . A portion of the local signal from Q_{3002} is fed through the buffer amplifier Q_{3003} (2SC535A) to the PLL local mixer, Q_{3004} (2SC535A), where the signal is mixed with a PLL local signal generated by Q_{3009} (2SC535A) and multiplied 7 times by Q_{3010} (2SC535A). This local signal varies from 131.9 – 131.999 MHz as a result of the control voltage from the CONTROL Unit. Thus, a PLL IF frequency of 2.00 – 5.99 MHz is obtained. The frequency varies at the PLL local signal, providing movement in 10 kHz steps.

This PLL IF signal is then amplified by Q_{3005} (2SC535A), and fed to Q_{3006} (μ PD2819C), where the programmable divider section divides the IF signal by 200-599, depending on the data from the 4-bit microprocessor in the CONTROL Unit.

Next, this signal is delivered to the phase comparator section, where the phase of divided IF signal is compared with its reference signal of 10 kHz. This reference signal is generated and divided by the reference oscillator/reference signal divider section in Q_{3006} . Any difference in phase of the divided PLL IF signal with that of the PLL reference signal is converted into an error-signal with a different bandwidth of pulse. This signal is then fed to active lowpass filters Q_{3007} and Q_{3008} , resulting in a VCO correction voltage.

When the PLL is unlocked, an unlock signal at pin 7 of Q_{3006} drops to a low level, cutting off the bias voltage at Q_{2009} (2SC2603E), and thus turning off Q_{2019} (3SK59GR) and Q_{1010} (3SK73Y).

ALC Circuit

A portion of the RF signal is coupled through C_{2037} to a rectifier circuit consisting of D_{2007} and D_{2008} (1S188FM), producing a DC voltage. The DC voltage is amplified by DC amplifier Q_{1014} (2SC3603E) and fed to gate 2 of Q_{1010} to control its gain, thus preventing overdrive. The ALC level is adjusted by VR_{2003} for proper gain at Q_{1010} .

Power Control Circuit

When the HI/LOW switch is set to the low position and the base of Q_{2007} (2SC2603E) to a high level, the voltage at the corrector becomes low, thus reducing the voltage at gate 2 of Q_{2019} (3SK59GR) and the amplitude of the RF signal.

PLL Control Circuit

The PLL Control Unit features a low current drain 4-bit microprocessor chip, Q_{5001} (HD44820A-62), which processes data for controlling the operating frequency, UP/DOWN scanning, priority channel, memory selection, etc. The CPU processes input data by means of the main dial or other control switches in accordance with the program stored in a ROM for control of the PLL frequency, indication of the operating frequency, or memory channels on digital display.

MAINTENANCE AND ALIGNMENT

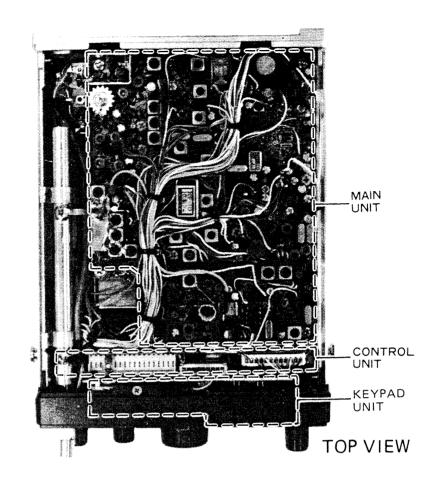
This equipment has been carefully aligned and tested at the factory prior to shipment. If the instrument is not abused, it should not require other than the usual attention given to electronic equipment.

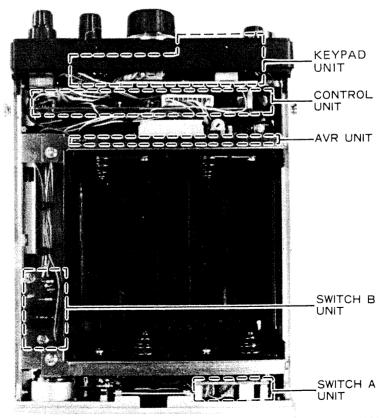
Service or replacement of a major component may require considerable realignment. Under no circumstances, though, should realignment be attempted unless the operation of the transceiver is fully understood, the malfunction has been carefully analyzed, and the fault has definitely been traced to misalignment rather than part failure. Service work must only be performed by experienced personnel using the proper test equipment.

Never align this transceiver without having a 50 ohm dummy load connected to the antenna jack. Troubleshooting using an antenna can result in misleading indications on the test equipment.

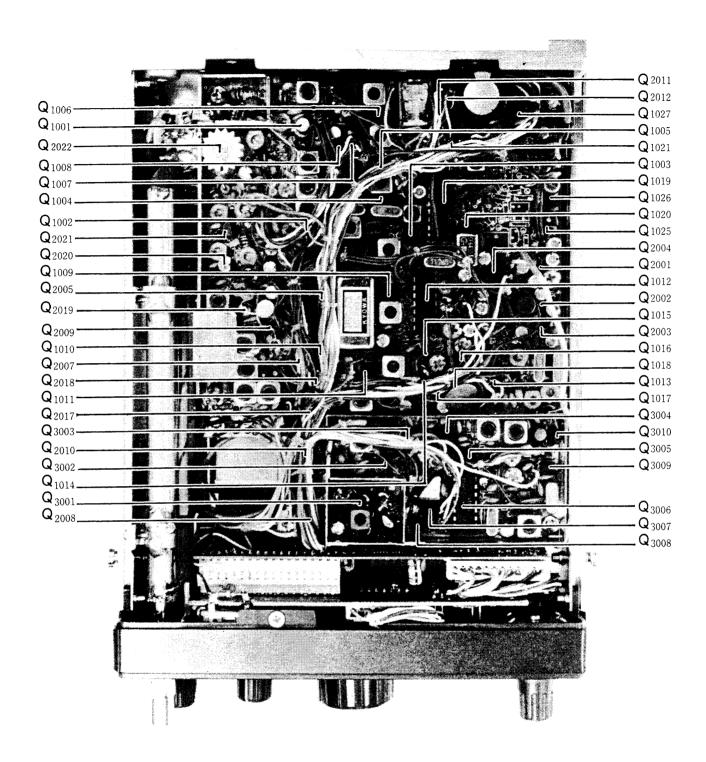
EQUIPMENT REQUIRED

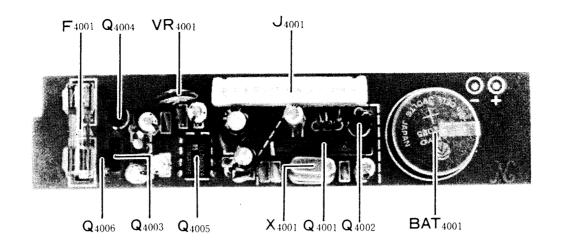
- 1. RF Signal Generator: Hewlett-Packard Model 8640B or equivalent with one volt output at 50 ohms and frequency coverage to 150 MHz.
- 2. Vacuum Tube Voltmeter (VTVM): Hewlett-Packard Model 410B or equivalent.
- 3. Dummy Load/Wattmeter: Yaesu YP-150Z or equivalent.
- 4. AF Signal Generator: Hewlett-Packard Model 200AB or equivalent.
- 5. IF Sweep Generator: capable of output at 10.81 MHz.
- 6. RF Sweep Generator: capable of output at 143 149 MHz.
- 7. Oscilloscope: Hewlett-Packard Model 1740A or equivalent.
- 8. FM Deviation Meter: coverage to 144 148 MHz.
- 9. Precision Frequency Counter: Yaesu Model YC-500E or equivalent with resolution to 0.01 kHz and frequency coverage to 150 MHz.

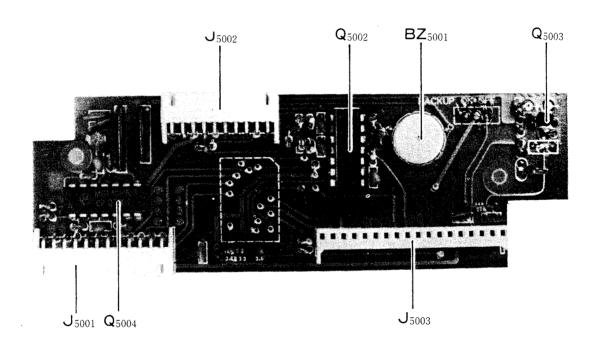


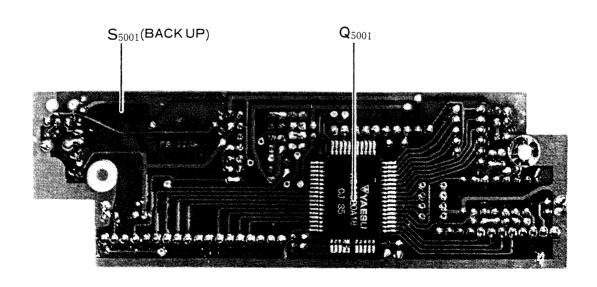


BOTTOM VIEW









PLL CIRCUIT ALIGNMENT

The PLL circuit alignment procedure is very critical because of the ambient temperature change. This alignment must be performed under temperature conditions between the range of 15 to 30°C. If your transceiver is exposed to temperatures beyond this range for an extended period of time, the transceiver should sit in the proper alignment temperature for at least two hours before you start the following alignment procedure.

1. PLL Local, IF Alignment

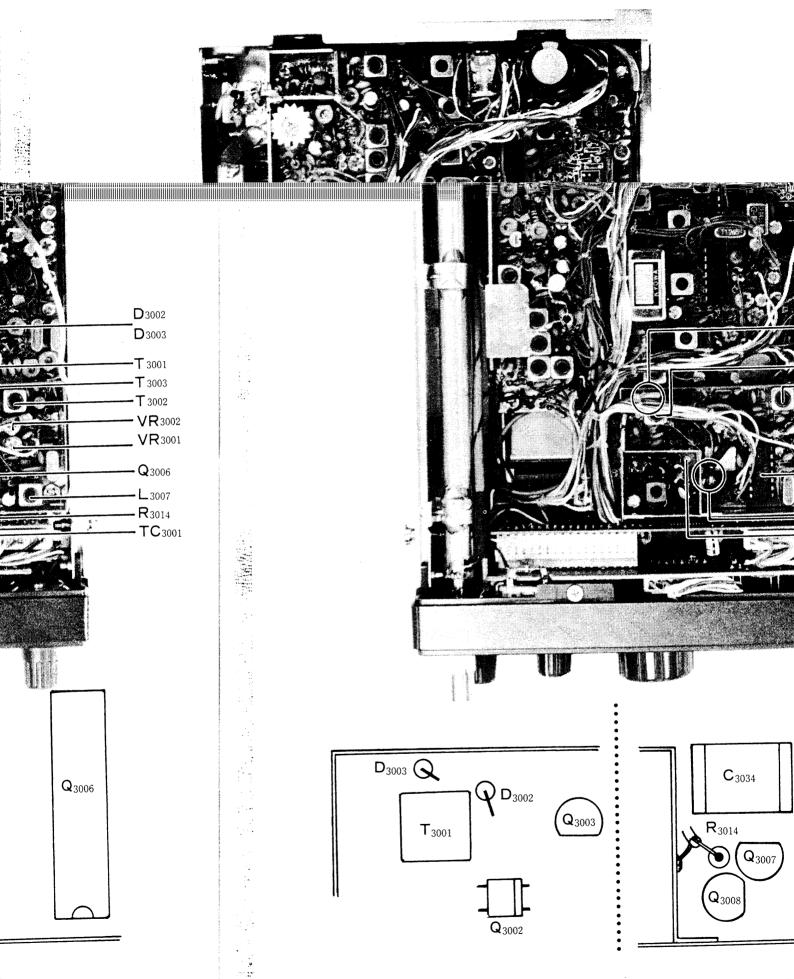
- (a) Set the MODE switch to the FM position, and adjust the frequency to 146.000.0 MHz (model B; 145.000.0 MHz).
- (b) Set TC_{3001} to the center position, and connect an oscilloscope to pin 14 of Q_{3006} .
- (c) Adjust the cores of $T_{3001} T_{3003}$ for maximum amplitude on the oscilloscope.

2. VCV Line Adjustment

- (a) Tune the transceiver to 146.000.0 MHz (model B: 145.000.0 MHz), and connect a DC voltmeter to R_{3014} .
- (b) Adjust TC_{3001} for a reading of 3.5 volts on the meter.

3. PLL Local Frequency Adjustment

- (a) Set the MODE switch to FM and tune the transceiver to $146.000.0 \, \text{MHz}$ (model B: $145.000.0 \, \text{MHz}$). Preset VR_{3001} and VR_{3002} to the center position.
- (b) Connect a frequency counter to the cathode of D_{3002} or D_{3003} .
- (c) Adjust the core of L_{3007} for the frequency of 135.190.0 MHz (model B: 134.190.0 MHz).
- (d) Now turn the CLAR switch on, and rotate the main knob one click counterclockwise (1 step).
- (e) Adjust VR_{3001} and VR_{3002} for a frequency of 135.189.9 MHz (model B: 134.189.9 MHz).
- (f) Repeat the alignment from step (c) to (e) a few times to be sure the proper frequency is obtained.

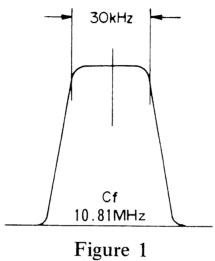


PLL SECTION ALIGNMENT POINTS

RECEIVER ALIGNMENT

First IF Alignment 1.

- Set the MODE switch to FM. (a)
- Connect a sweep generator output to gate 1 of Q_{1002} and set the (b) frequency of the sweep generator to 10.81 MHz... Connect an oscilloscope, through a detector, to pin 16 of Q_{1019} .
- Adjust the cores of T_{1005} , T_{1006} and T_{1014} until the scope pattern (c) illustrated in Figure 1 is obtained.



FM Discriminator Alignment 2.

- (a) Set the MODE switch to FM and rotate the SOL control fully counterclockwise.
- Connect an audio voltmeter to the speaker terminal. (b)
- Set the VOL control to the center position, and adjust the core of (c) T_{1013} for a maximum reading on the meter.

3. SSB/CW IF Alignment

- Set the MODE switch to CW and the frequency to 146 MHz (model B: (a) 145 MHz).
- Connect an RF signal generator to the ANT jack and set the output (b) level and frequency to 15 dB μ at 146 MHz (model B: 145 MHz).
- Adjust the cores of T_{1006} , T_{1010} and T_{1011} for maximum reading on (c) the S-meter.

4. RF Coil Alignment

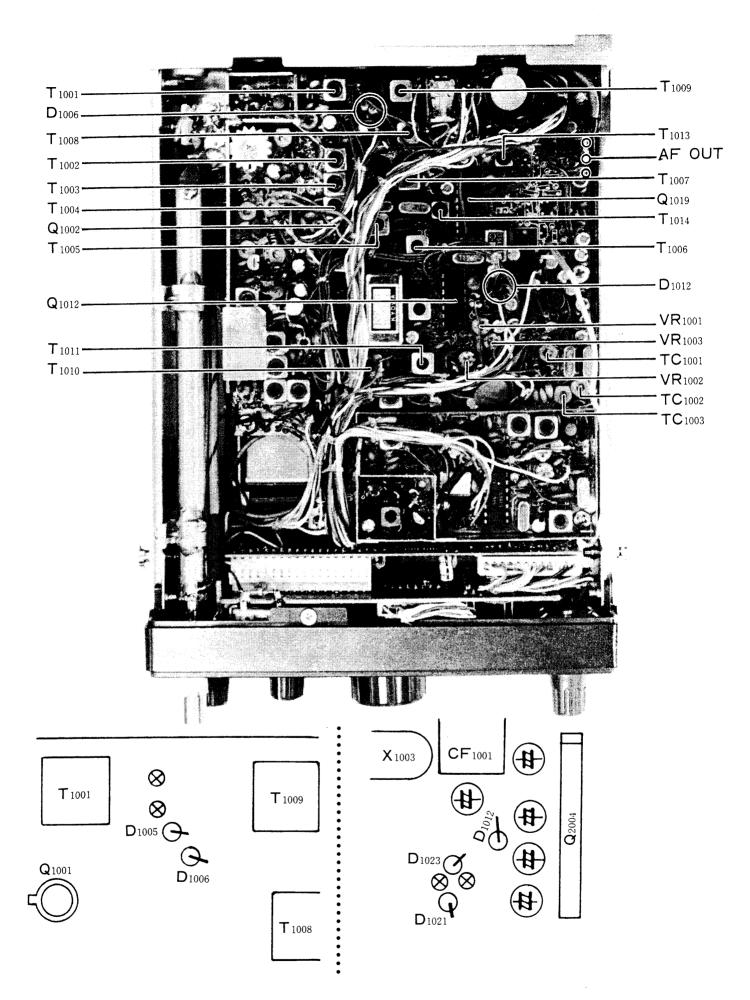
- (a) Set the output level and frequency of the generator to $10~\mathrm{dB}\mu$ at 146 MHz (model B: 145 MHz).
- (b) Set the receiver frequency to 146 MHz (model B: 145 MHz), and adjust the cores of T_{1001} T_{1004} for maximum deflection on the S-meter.

5. S-meter Alignment

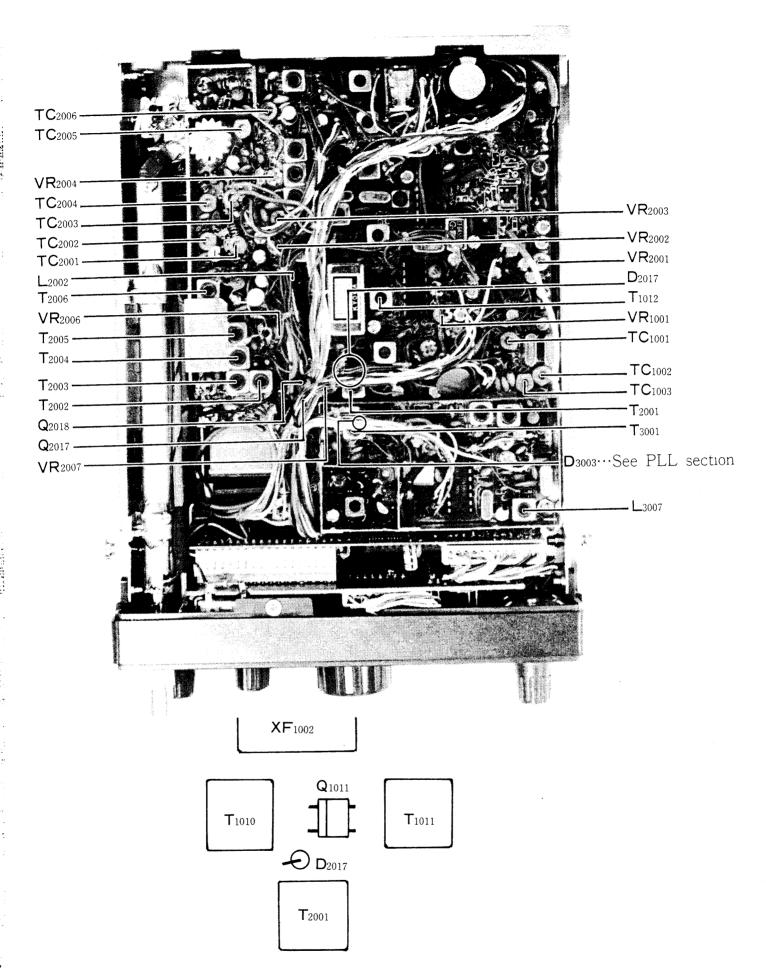
- (a) Set the MODE switch to USB or LSB and preset VR_{1001} to the center position.
- (b) Apply a 15 dB μ signal from the signal generator, and adjust VR₁₀₀₃ for a reading of S9 on the S-meter.
- (c) Now remove the signal from the signal generator, and adjust VR_{1002} so the S-meter indicates exactly 0.
- (d) Repeat steps (b) and (c) a few times to obtain the proper S-meter deflection.

6. N.B. Alignment

- (a) Set the MODE switch to CW and apply a 5 dB μ signal from the signal generator.
- (b) Connect the \oplus lead of a DC voltmeter to the cathode of D_{1006} and the \bigcirc lead to the -6.8 volts line.
- (c) Adjust the cores of $T_{1007} T_{1009}$ for maximum deflection on the voltmeter.
- (d) Next, reduce the amplitude of the signal generator to 0 $dB\mu$, and check the voltmeter, which should show approximately 0.03 volts.



RECEIVER SECTION ALIGNMENT POINTS



TRANSMITTER SECTION ALIGNMENT POINTS

TRANSMITTER

The transmitter alignment should be performed with a dummy load connected to the antenna jack.

1. RF Power Stage Alignment

- (a) Tune the transceiver to 146.000 MHz (model B: 145.000 MHz), and set the MODE switch to FM. Connect a dummy load/wattmeter to the ANT jack.
- (b) Rotate VR_{2003} and VR_{2004} fully counterclockwise, and close the PTT switch.
- (c) Connect the probe of a VTVM to the cathode of D_{3003} and check to see that the VTVM shows approximately 500 mV rms.
- (d) Connect the probe of the VTVM to the cathode of D_{2017} and a frequency counter to the same point.
- (e) Adjust the core of L_{2002} for a reading of 10.81 MHz ±100 Hz, and be sure its level is approximately 500 mV rms.
- (f) Now adjust $T_{2001}-T_{2006},\ T_{3001},\ TC_{2001}-TC_{2006}$ for maximum reading on the wattmeter.

2. ALC Alignment

- (a) Set the MODE switch to FM, and close the PTT switch.
- (b) Adjust VR_{2003} for a reading of 2.5 watts on the wattmeter.

3. PO Meter Alignment

- (a) Set the MODE switch to FM and close the PTT switch.
- (b) Adjust VR_{2004} so that the PO meter indicator reaches the middle of the green zone, with a 2.5 watt reading on the wattmeter.

4. FM Deviation Alignment

- (a) Assemble the test equipment and the transceiver as shown in Figure 2.
- (b) Connect an audio generator to the MIC jack, and apply a 1 kHz 15 mV signal.

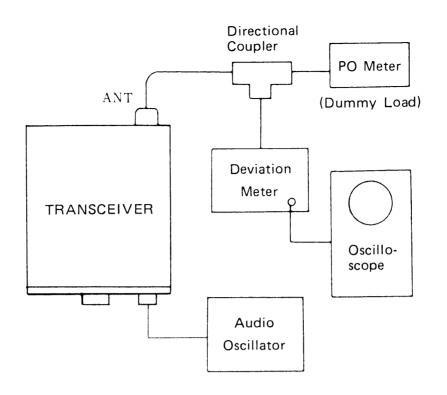


Figure 2

- (c) Now close the PTT switch, and adjust VR_{2002} for a deviation of ± 4.5 kHz while observing the signal waveform on the scope.
- (d) Reduce the amplitude of the audio generator to 1.5 mV, and check to see that the linear detector shows ±3.5 kHz and that the signal waveform on the scope is not distorted.

5. Low Power Adjustment

- (a) With the dummy load/wattmeter connected to the ANT jack, set the MODE switch to FM and the HI/LOW switch to the LOW position.
- (b) Close the PTT switch and adjust VR_{2006} for an output of 0.5 watts.

6. SSB Modulator Output Transformer Adjustment

- (a) With a dummy load/wattmeter connected to the ANT jack, set the MODE switch to either USB or LSB.
- (b) Set VR_{2001} to the center of its range and apply a 1 kHz 1 mV signal from the audio generator to the MIC jack.
- (c) Adjust T_{1012} for maximum power output.

7. SSB Carrier Point Adjustment

- (a) Apply a 1 kHz 1.2 mV signal from the audio generator to the MIC jack and adjust VR_{2001} for an output of 2.5 watts.
- (b) Set the MODE switch to USB and the frequency of the audio generator to 300 Hz. Adjust TC_{1002} for an output of 0.6 watts.
- (c) Change the MODE switch to LSB, and adjust TC_{1001} for an output of 0.6 watts.

8. Carrier Balance Adjustment

- (a) Temporarily short the PTT line at the MIC jack, using a jumper wire, not the microphone.
- (b) Monitor the carrier on a monitor receiver, and adjust VR₁₀₀₁ for minimum S-meter reading (or minimum signal level if no S-meter reading occurs).
- (c) Switch the MODE switch between USB and LSB, and compare the carrier levels of both modes and again adjust VR_{1001} so as to achieve good carrier nulling on both modes.

9. CW Carrier Frequency Adjustment

- (a) Set the MODE switch to CW.
- (b) Connect a frequency counter to the cathode of D_{2017} .
- (c) Connect a CW key to the KEY jack, and then close the PTT switch and KEY simultaneously. Adjust TC_{1003} for a frequency of exactly 10.8093 MHz.
- (d) Now set the frequency to 145.100.0 MHz and place the input lead from the counter to the dummy load to read the transmit frequency. Then adjust L_{3007} for a reading of 145.100.0 MHz ±100 Hz on the frequency counter.

10. TX Balanced Mixer Alignment

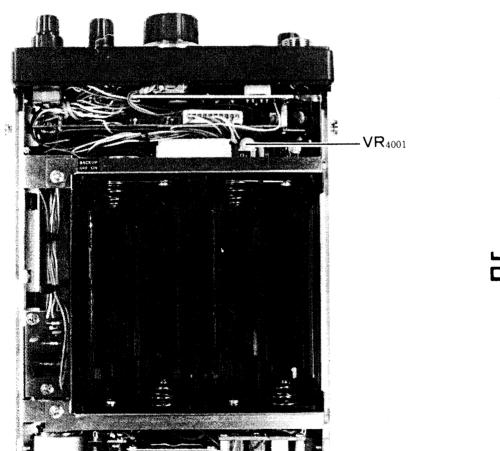
If you do not have a spectrum analyzer, do not perform this alignment, as serious spurious radiation will result.

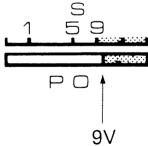
(a) Connect a directional coupler between the transceiver and dummy load/wattmeter, and feed the coupled output from the directional coupler to the spectrum analyzer.

(b) Set the MODE switch to FM and close the PTT switch. Adjust VR_{2007} so that a minimum spurious level appears ± 10.81 MHz from the carrier on the spectrum analyzer.

11. Battery Check

- (a) Apply a DC 9V to the EXT DC 13.8V terminal from an external power supply.
- (b) Set the LAMP/BATT CHECK switch (on the REAR PANEL) to the BATT CHECK position.
- (c) Adjust VR_{4001} so that the PO meter deflects to the left side of the green zone.





PARTS LIST

		MAIN CHASSIS
Symbol No.	Part No.	Description
		DIODE
D01, 02	G2090001	Si 10D1
D03	G2090027	Si 1SS53
D04	G2090034	Si U05B
		RESISTOR
R02	J01215101	Carbon Film 1/8W TJ 100Ω
R01	J01215103	" " " 10kΩ
		POTENTIOMETER
VR01 (with S01)	J62800057	K12B61004-5N1211-5KB, 10KA
	·	CAPACITOR
C03	K00175150	Ceramic Disc 50WV SL 15pF
		(DD104SL150J50V)
C04	K00175390	Ceramic Disc 50WV SL 39pF
		(DD104SL390J50V)
C01, 02, 05, 06	K12171102	Ceramic Disc 50WV 0.001µF
		(DD104E102P50V)
C07-10	K10179016	Ceramic Disc 50WV 0.001µF
		(DB201YB102K5L5)
C11	K40129011	Electrolytic 16WV 1000 μ F
		(16RE1000)
		INDUCTOR
L01	L0020951	
L02	L0020334	
		SWITCH
S01 (with VR01)	′ –	
S02	Q9000115	EWT-XDBS2050B
S03	N0190082	SRN3066
S04	N0190084	SRS101C Switch Unit (C)
PB-2240	F0002240	Printed Circuit Board
	C022400A	PCB with S04
S05	N6090028	SSHP-23-05 Switch Unit (A)
S06, 07	N6090029	SSFYP-22-07 Switch Unit (A)
PB-2242	F0002242	Printed Circuit Board
	C022420A	PCB with S05, S06, S07
		300, 500, 507
		RECEPTACLE
J01	P0090243	FM214-7SS(A)
J02	P1090193	FM-MR-M

J03, 07	P1090005	SG8050
J04	P1090051	SG8512
J05	P0090190	HEC0630
J06	P1090197	SG8021
		SPEAKER
SP01	M4090053	SM-50Y
		CONNECTOR
P01 (with wire)	T9204140	XHP-9
P02 (with wire)	T9204150	XHP-10
P03 (with wire)	T9204160	XHP-12
P04 (with wire)	T9204247B	XHP-13
P05 (with wire)	T9204400	(Tone SQL)
		ANTENNA
ANT01	Q3000020	
		METER
M01	M0290023	T-22
		BATTERY HOLDER
	Q9000116B	C-12A (with wire)
	Q9000110B	C-12A (with wire)
	Q3000117B	
		KNOB
	R3068750	FT-32T Main Tuning
	R3068780	FT-14K MODE, M.CH
	R3068790	FT-12K VOL
	R3068800	FT-16DS SQL
	R3062161	Push Button A FUNCTION
	R3056500	Push Button B VFO
		MAIN UNIT
Cumbal Na	Part No.	Description
Symbol No. PB-2235C	F0002235C	Printed Circuit Board
19 22330	C022350A	PCB with Components Model A
	C022350B	" B, C, D
	C022350C	" F
		IC
Q1012	G1090340	MC1496P
Q1019	G1090145	MC3357P
Q1027	G1090073	μPC575C2
Q2004	G1090072	μPC577H
Q2010	G1090027	MC14001B
Q3006		

		FET		
Q1001	G4800590Y	3SK59Y		
Q1002	G4800510C	3SK51-03		
Q1003, 1010,	G4800730Y	3SK73Y		
1111, 3002				
Q1009, 3003	G3801680D/E	2SK168D/E		
Q1015, 2017,	G3801920G	2SK192GR		
2018, 3001	330017200			
Q3007	G3800301Y	2SK30A-Y		
Q2019	G4800590G	3SK59GR		
Q2017	040003700	35K39GK		
		TRANSISTOR		
Q1004, 3004,	G3305350A	2SC535A		
3005, 3009,				
3010				
Q1007, 1013,	G3326030E	2SC2603E		
1014, 1017,				
1020, 1021,				
1025, 1026,				
2001-2003,				
2007-2009				
Q1008	G3090005	MPS-A13		
Q1016	G3107331Q	2SA733Q		
Q1018	G3107331P	2SA733P		
Q2011, 2012,	G3309451P	2SC945P		
3008				
Q2020	G3320260	2SC2026		
Q2021	G3320530	2SC2053		
Q2022	G3319470	2SC1947		
Q1005	G3315830	2SC1583		
Q1006, 2005	G3327860	2SC2786L		
		DIODE		
D3006	G2090023	Varactor	1SV50	
D2005	G2090180	,,,	FC 53M-5	
D1002-1004,	G2090109	,,	1SV69	
2018-2022				
D3001	G2090107	"	1T25	
D1005, 1006,	G2001880F	Ge	1S188FM	
1028, 1029,				
1031, 1042,				
2007–2009,				
2010, 2027,				
3005	0001555			
D2023	G2015550	Si	1S1555	
D2006	G9090005	Varistor	MV103	

	G200007			
D1007-1024,	G2090027	Si	1SS53	
1027, 1030,				
1032, 1039,				
1041, 2011-				
2013, 2016,				
2017, 2026,				
2028, 2030,				
3002-3004,				
3009				
D2024, 2025	G2090033	Si	MI301	
D2029	G2090193	Zener	RD5.6EB-3	
D3007	G2090196	"	HZ6C-1L	
		CRYSTAL		
X1001	H0100992	HC-18/U	10.8115 MHz	
X1002	H0102288	HC-18/U	10.8093 MHz	
X1003	H0101100	HC-18/U	11.265 MHz	
X2001	H0102448	HC-18/U	10.810 MHz	
X3001	H0101986	HC-18/T	5.76 MHz	
X3002	H0102385B	RW-18/T3P	18.7414 MHz	
		CRYSTAL FILTER		
XF1001	H1102021	108M30B		
XF1002	H1102022	10F2D	10.81 MHz	
				A CONTRACT OF THE CONTRACT OF
		CERAMIC FILTER		
CF1001	H3900171	CFG455E-1/SLFD15	SA	
0				
		RESISTOR		
R1122	J10246229	Carbon Composition	1/4W GK	2.2Ω
R2069	J00215569	" Film	1/8W VJ	5.6Ω
R2066	J00215100	" "	11 11	10Ω
R2071	J00215470	" "	,, ,,	47Ω
R2059	J10246560	" Composition	1/4W GK	56Ω
R1018, 1037,	J00215560	" Film	1/8W VJ	56Ω
1038, 1121,			,	
2020, 2063				
R1012, 1021,	J00215101	11 11	11 11	100Ω
1023, 1026,				
1030, 1043,				
1048, 1052,				
1057, 2054,				
2067, 3003,				
3008, 3012,				
i i				
3019, 3040				

D1112 2211	T10046101	- ·		4 / 4==-			
R1113, 3044	J10246101		n Composition	1/4W	GK	100Ω	
R1005, 1015,	J00215151	,,	Film	1/8W	VJ	150Ω	
1041							
R1116, 2028,	J00215221	"	,,	"	"	220Ω	
3002, 3007	7100115						
R2075	J10246221	,,	Composition	1/4W	GK	220Ω	
R2072	J02245331	,,	Film	1/4W	SJ	330Ω	
R2070	J02245391		• • • • • • • • • • • • • • • • • • • •			390Ω	
R2013, 2014,	J00215471	"	,,	1/8W	VJ	470Ω	
2061, 3036,							
3039							
R1032, 1054,	J00215561	,,	"	"	"	560Ω	
1055, 2078							
R1065	J10246561	"	Composition	"	GK	560Ω	
R2068	J00215681	"	Film	,,	VJ	680Ω	
R3013	J00215821	"	/ 1	,,	"	820Ω	
R1033, 1034,	J00215102	,,	"	"	,,	lkΩ	
1036, 1044,							
1049, 1077,							
1107, 1118, 2007, 2027,							
2065, 3018,							
3031, 3035							
R1040	J10246102	•	Composition	1/4W	GK	lkΩ	
R3045	J00215122	,,	Film	$\frac{1/4W}{1/8W}$	VJ	1.2kΩ	
R1076, 1081,	J00215152		,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,,	1/6**	,, ,,		
1087, 2019,	300213132					1.5 k Ω	
3017, 3030							
R3041	J10246222		Co	1 / 4777	CII	0.61	
R1019, 1031,	J10246222 J00215222		Composition	1/4W	GK	2.2kΩ	
1058, 1083,	300213222		Film	1/8W	VJ	$2.2k\Omega$	
2012, 2018,							
3028, 3029							
R1042,	100215272						
1	J00215272	••		"	"	2.7 k Ω	
3009-3011	110246272						
R3020	J10246272	· ''	Composition	1/4W	GK	2.7kΩ	
R1046, 1056,	J00215332	,,	Film	1/8W	VJ	$3.3k\Omega$	
1062, 1102,							
2001, 2003,							
2039 P1053, 2008	100215202		,,				
R1053, 2008	J00215392			"		3.9kΩ	
R1051, 1061,	J00215472	′′	"	"	"	$4.7k\Omega$	
1070, 1072,							
1086, 1106,							
2004, 2009,							
2034, 2049,							
2064, 3027							
							- 4

R1078	J10246472	,,	Composition	1/4W	GK	4.7kΩ
R1103, 2021	J00215562	Carbo	n Film	1/8W	VJ	5.6kΩ
R1103, 2021	J00215682	(1)	,,	1/011	11	6.8kΩ
R1059, 2005	J00215822	,,	,,	,,	,,	8.2kΩ
	J10246103	,,	Composition	1/4W	GK	10kΩ
R1001, 2037,	J10240103		Composition	1/7***	GK	10875
2045, 3014 P1002, 1011	J00215103	,,	Film	1/8W	VJ	10kΩ
R1002, 1011,	J00213103		1,11111	1/0**	V 3	10K75
1025, 1029,						
1050, 1068,						
1071, 1089,						
1100, 1104,						
1105, 1109, 2042, 2044,						
2060, 2073,						
2074, 3016						
R2010	J00215123	"	,,	,,	,,	12kΩ
R1060, 1080,	J00215123	,,	,,	,,	,,	15kΩ
1082	300210100					
R2076	J10246153	,,	Composition	1/4W	GK	15kΩ
R1010, 1063,	J00215223	"	Film	1/8W	VJ	22kΩ
1069, 1098,						
2011, 2033,						
2035, 2077, 3004, 3005,						
3015, 3033,						
3037						
R2036	J01215223	,,	,,	"	TJ	22kΩ
R1114, 1115,	J10246223	"	Composition	1/4W	GK	$22k\Omega$
2031, 2038						
R3026	J10246333	"		"		33kΩ
R1125,2023,2043	J00215333	,,	Film	1/8W	VJ	33kΩ
R1016, 1035,	J00215473	"	,,	"	"	47kΩ
1039, 1064,						
1075, 1084,						
1085, 1090,						
1110, 2022,						
2032, 2050,						
2051, 3001,						
3034, 3038	T00015600					601-0
R2025	J00215683	"				68kΩ
R1073, 1074	J00215823	,,		",		82kΩ
R1006, 1008,	J00215104				•	100kΩ
1009, 2052,						
2053, 2055-						
2058, 2062,						
3006, 3032						
R3022-3024	J10246104	,,	Composition	1/4W	GK	100kΩ
R1112	J00215124	,,	Film	1/8W	VJ	120kΩ
1 -	· ·	i		,		·

R1024, 1111,	J00215154	Carbon Film	1/8W V	J	150kΩ
3021	100015001	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,, ,,		2201.0
R1119	J00215224	,, ,,			220kΩ
R1079, 3043	J00215274	,, ,,	·· ·· ·· ·· ·· ·· ·· ·· ·· ·· ·· ·· ··		270kΩ
R2006, 3042	J00215334	,, ,,			330kΩ
R1020, 1027	J00215474	,, ,,	,, ,,		470kΩ
1108, 1120	700017001	,, ,,	,, ,,		(001 -
R2040	J00215684	" "			680kΩ
R1004,1123,3025	J00215105	" "		2	1ΜΩ
R1066, 1097	J00215155		" V.		1.5ΜΩ
R2002	J00215225	" "			2.2ΜΩ
R1067	J00215335	" "			3.3MΩ
R1124	J00215331		,, ,,		330Ω
R1126	J01215102	" "	" T.	J	1kΩ
					· · · · · · · · · · · · · · · · · · ·
TIDOGG		POTENTIOMETER	100		
VR2008	J51745471	H0651A005-470B	470ΩB		
VR2006, 2007	J51745102	H0651A007-1KB	1kΩB		
VR2002	J51745332	H0651A010-3.3KB	3.3kΩB	· · · · · · · · · · · · · · · · · · ·	
VR2003, 2004	J51745103	H0651A013-10KB	10kΩB		
VR1002, 2001	J51745223	H0651A015-22KB	22kΩB		
VR1001, 3002	J51745473	H0651A017-47KB	47kΩB		
VR3001	J51745154	H0651A020-150KB	150kΩB		
VR1003	J51745105	H0651A027-2.2MB	1MΩB		
		THERMISTOR			
TH2001	G9090020	21D27			
TH3001	G9090008	31D26			
					
		CAPACITOR			
C1015,2037	K00179001	Ceramic Disc	50WV	SL	0.5pF
2072,3046		(DD104SL0R5C50V)			•
C2038	K00173010	" "	,,	SL	1pF
02000		(DD104SL010C50V)			
C2076, 2083	K02179003	11 11	,,	СН	2pF
02070, 2003	1102117003	(DD104CK020C50V)		U11	~r^
	K00172020	" "	11	SL	2pF
	1100112020	(DD104SL020C50V)		~~	~r~
C3004	K06172020	" "		UJ	2pF
	1100112020	(ECC-D1H020CU)		\cup \mathbf{j}	2p1
C1012 2070	K02179004	(ECC-D111020C0)		CII	2 - E
C1013, 2070,	NU21/3004	(DD104CH020C50V)		СН	3pF
2074, 3006		(DD104CH030C50V)			
C3017	K00172030	" "	"	SL	3pF
		(DD104SL030C50V)		JL	op.
C2030	K06172040	" "	"	UJ	4pF
22030		(ECC-D1H040CU)		<u> </u>	' ۲*
L		(200 21110 1000)			

K00172050	Ceramic Disc	50WV	SL	5pF
K06172050	11 11	"	UJ	5pF
K00173060	11 11	,,	SL	6pF
K06173060	(DD104UJ060D 50V)	"	UJ	6pF
K06173070	,, ,, (ECC-D1H070DU)	,,	,,	7pF
K00173070	,, ,, (DD104SL070D 50V)	,,	SL	7pF
K05173080	,, ,, (DD104RH080D 50V)	,,	RH	8pF
K06173080	,, ,, (DD104UJ080D50V)	,,	UJ	8pF
K00173090	(DD104SL090D 50V)	,,	SL	9pF
K06173100	,, ,, (DD104UJ100D50V)	,,	UJ	10pF
K00173100	(DD104SL100D 50V)	"	,,	10pF
K02173100	,, ,, (DD104CH100D 50V)	,,	СН	10pF
K02175120	,, ,, (DD104CH120J 50V)	••	"	12pF
K06175150	,, , ,, (ECC-D1H150JU)	,,	UJ	15pF
K02175180	,, ,, (DD104CH180J 50V)	**	СН	18pF
K00175180	,, ,, (DD104SL180J50V)	"	SL	18pF
K00175220	(DD104SL220J50V)	"	SL	22pF
K02179011	,, ,, (DD104CH270J 50V)	"	СН	27pF
K00175330	(DD104SL330J 50V)	.,	SL	33pF
K00175390	(DD104SL390J 50V)	"	"	39pF
K00175470	(DD104SL470J 50V)	""	,,	47pF
K00175560	(DD104SL560J 50V)	"	SL	56pF
	K06172050 K00173060 K06173060 K06173070 K00173070 K05173080 K06173090 K06173100 K02173100 K02175120 K06175150 K02175180 K00175220 K02179011 K00175390 K00175470	(DD104SL050C 50V) K06172050 " " (DD104RH050D 50V) K00173060 " " (DD104SL060D 50V) K06173070 " " (ECC-D1H070DU) K00173070 " " (DD104SL070D 50V) K05173080 " " (DD104H080D 50V) K06173080 " " (DD104UJ080D50V) K06173090 " " (DD104SL090D 50V) K06173100 " " (DD104SL100D 50V) K02173100 " " (DD104CH100D 50V) K02175120 " " (DD104CH120J 50V) K06175180 " " (DD104CH180J 50V) K00175220 " " (DD104SL180J 50V) K00175330 " " (DD104SL220J 50V) K0175330 " " (DD104SL330J 50V) K00175390 " " (DD104SL390J 50V) K00175390 " " (DD104SL390J 50V) K00175560 " " (DD104SL470J 50V)	(DD104SL050C 50V) K06172050 (DD104RH050D 50V) K00173060 (DD104UJ060D 50V) K06173060 (DD104UJ060D 50V) K06173070 (ECC-D1H070DU) K00173070 (DD104SL070D 50V) K05173080 (DD104RH080D 50V) K06173080 (DD104RH080D 50V) K06173090 (DD104SL090D 50V) K06173100 (DD104SL090D 50V) K00173100 (DD104SL100D 50V) K02173100 (DD104CH120J 50V) K02175120 (DD104CH120J 50V) K00175180 (DD104CH180J 50V) K00175180 (DD104SL180J 50V) K00175220 (DD104SL180J 50V) K00175330 (DD104SL220J 50V) K00175330 (DD104SL330J 50V) K00175330 (DD104SL330J 50V) K00175390 (DD104SL390J 50V) K00175390 (DD104SL390J 50V) K00175560 (DD104SL470J 50V) K00175560 (DD104SL470J 50V)	(DD104SL050C 50V)

Grade	7500477404				
C1076	K02175101	Ceramic Disc (DD107CH101J 50V)	50WV	СН	100pF
C1053, 1127	K00179056	(DD105-257SL101J50V)	"	SL	100pF
C3035, 3038, 3039	K06175101	(DD106UJ101J50V)	,,	UJ	100pF
C1096	K00175121	(DD105SL121J50V)	"	SL	120pF
C2031	K02179023	(DD110CH181J50V)	"	СН	180pF
C2032	K06175181	(DD104UJ181J50V)	"	UJ	180pF
C1075	K02179025	,, ,, (DD111CH221J50V)	"	СН	220pF
C1041	K00175331	" " (DD107SL331J50V)	"	SL	330pF
C1001, 1002, 1004, 1006, 1008, 1031, 1048, 1066, 1130, 2002, 2003, 2014, 2018, 2027, 2036, 2039, 2040, 2046, 2058, 2059, 2062, 2063, 2068, 2069, 2079—2081, 2084, 2085, 2088, 2090— 2092, 2094, 2097, 2100, 2102, 2105, 2107, 2108, 2111, 2113, 3009, 3011— 3013, 3015, 3016, 3018, 3020, 3023, 3028, 3041, 3044, 3049, 3050, 3051— 3053, 3057— 3059	K12171102	" (DD104E102P50V)			0.001µF
C1011, 2033–2035	K12179001	(DD104-257E102P50V)	,,		0.001µF
C1005, 1007, 1018, 1061, 1073, 1074, 1080–1082, 1084, 1088– 1090, 1093, 2054, 2060 2061, 3029	K13179008	(DD104-237E102F30V) (DD106F103Z50V)			0.01μF

C3026, 3030, 3037, 3042	K14179002	Ceramic Disc (RD204YM103Z50V)		0.01μF
C1024, 1098,	K19149001	Semiconductor Ceramic	25WV	0.001μF
1099, 1133,		(UAT04X102K-L05AE)		
2008, 2041				
C1124	K19149005			0.0022μF
		(UAT04X222K-L05AE)		
C2009	K19149007	"	,,	0.0033µF
		(UAT05X332K-L05AE)		
C1044, 1049,	K19149013	"	,,	0.01µF
1054, 1060,		(UAT05X103K-L05AE)		
1063, 1065,				
1068, 1069,				
1071, 1108,				
1125, 2010,				
2012, 2017,				
2043				
C1100, 1102	K19149017	"	"	$0.022 \mu F$
		(UAT06X223K-L45AE)		
C2024, 2025	K19149019	"	"	$0.033\mu\mathrm{F}$
		(UAT08X333K-L45AE)		
C1021, 1025,	K19149021	"	"	$0.047 \mu F$
1027, 1028,		(UAT08X473K-L45AE)		
1030, 1034,				
1035, 1037,				
1039, 1043,				
1045, 1047,				
1050, 1052,				
1055, 1057,				
1085, 1105,				
1106, 1109,				
1110, 1138,				
2044, 2045,				
3003				
C1091, 1135	K19149025	"	,,	0.1μF
01071, 1133	1111111020	(UAT13X104K-L46AE)		- · - , · · · · ·
C1097	K40179002	Electrolytic	50WV	0.1µF
	12.02,7002	(ECE-A1HK 0R1)	= = *	
C1042, 1067,	K40179001	"	""	1μF
1086, 1097,		(ECE-A1HK010)		
1111, 1112,				
1126, 1129,				
2001, 2005,				
2006, 2013,				
,,	1			

	T	1		4.5.
C1092, 1094,	K40149011	Electrolytic	25WV	4.7µF
1131, 3048		(ECE-A1EK4R7)		
C1062, 1064,	K40129012	"	16WV	$10\mu F$
1087, 1104,		(ECE-A1CK100)		
2004, 2007,				
2011, 2019,				
2021–2023,				
2086, 2089,				
2093, 2096,				
2109, 2110,				
3010, 3045,				
1				
3054, 3055				
	77.401.00000	"	1.600	47 F
C1132	K40129002	(16RE47)	16WV	47μF
G1120 1127	W.40100003	(TORL+7)	101111	47 F
C1128, 1137	K40109002		10WV	47μF
		(10RE47)		100 5
C1134	K40129007		16WV	100μF
		(16RE100)		
C1136	K40129021	"	"	1000µF
		(16R102S)		
C3033	K70167474	Tantalum	35WV	$0.47\mu\mathrm{F}$
		(CS15E1VR47)		
C2020	K70120002	"	16WV	10µF
		(489D106x0016C7)		
C3034	K54200001	Polyester Film	100WV	$1\mu F$
		(B32561-A-1105J)		
		TRIMMER CAPACITOR		
TC3001	K91000056	TZ03Z070A	7pF	
TC1001-1003,	K91000075	TZ03R200E	20pF	
2001-2006				

		INDUCTOR		
L3003	L1190004	FL 4H-R68M	0.68µH	
L2011, 3004,	L1190005	FL 4H-1R0M	1μΗ	
,,			•	
L1003	L1190111	FL 4H-5R6K	5.6µH	
L3001, 3006	L1190014	FL 4H-100K	10μΗ	
L1001	L1190014	FL 5H-101K	100µH	
L1001 L1004, 1005	L1190120	FL 5H-471K	470µH	
L2001	L1190120	S-154K	100mH	
L2001	L1020682	N-17-IV	1001111	
				
L2003, 2005	L1020683			
L2007, 2009	L1020681			
L3005	L1020680			
L2002	L0021166			
L2006	L0020725			

L2008	L0020766	
L2010	L0020744	
L2012	L0020341	
L2013, 2014	L0020743	
L3002	L0020359A	S-6B
L3007, 3008	L0020950	
		TRANSFORMER
T1001-1004,	L0020345	
2002-2006,		
3001-3003		
T1005-1012	L0021162	
T1013	L0020887	
T2001	L0020910A	
T1014	L0020888	
A CONTRACTOR OF THE CONTRACTOR		RELAY
RL2001	M1190035	FBR211AD006M
		TERMINAL BOARD
	Q6000005	1L2P
772221 2002	10100001	
FB2001, 2002	L9190001	Ferrite Beads
		REG UNIT
Symbol No.	Part No.	Description
PB-2396A	F0002396A	Printed Circuit Board
10 237071	C023960A	P C B with Components
	002070022	1 C 2 William Components
		IC
Q4001	G1090239	TC5082P
Q4005	G1090350	ICL7660CPA
		FET
Q4006	G3801930K	2SK193K
-		
		TRANSISTOR
Q4002	G3307331P	2SA733P
Q4003	G3304960Y	2SC496Y
<u></u>		

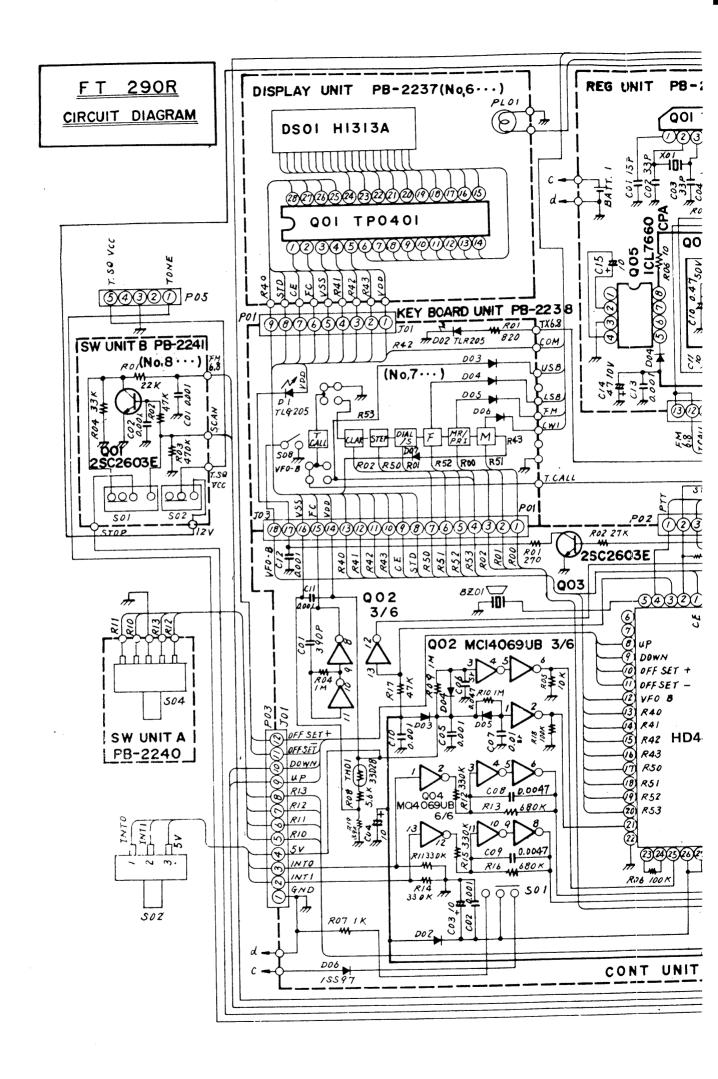
Q4004	G3309451P	2SC945P			
		DIODE			
D4001, 4004-4006	G2090027	Si	1SS53		
D4002	G2090104	Zener	RD6.8	EB-3	
D4003	G2090193	"	RD5.6	EB-3	
X4001(1750H-)	110101002	CRYSTAL	Model	D C I) E
X4001(1750Hz)	H0101982	HC-18/T 7.168 MHz			J.E.
X4001(1800Hz)	H0101983	HC-18/T 7.3728 MHz	Model	A	
		RESISTOR			
R4006	J00215100	Carbon Film	1/8W	VJ	10Ω
R4005	J00215471	,, ,,	"	.,	470Ω
R4007	J00215102	" "		•••	1kΩ
R4001	J00215222	" "		,,	2.2kΩ
R4002, 4003	J00215103	" "		•	10kΩ
		POTENTIOMETER			
VR4001	J50717104	RV8-HAS 100K	100ks	ΣВ	
		CAPACITOR			
C4001	K00175150	Ceramic Disc	50WV	SL	15pF
		(DD104SL150J50V)			
C4002, 4003	K00175330	" . <i>"</i>	,,	"	33pF
		(DD104SL330J50V)			
C4022	K13179008	" "	,,	F	$0.01 \mu \mathrm{F}$
		(DD106F103Z50V)			
C4007, 4009,	K12171102	" "	"	E	$0.001 \mu F$
4012, 4013,		(DD104E102P.50V)			
4016, 4017	V. 101 - 00 - 0	71			
C4006, 4010	K40179005	Electrolytic	"		$0.47\mu\mathrm{F}$
G4004 4005	W40170001	(ECE-A1HKR47)			1 1
C4004, 4005	K40179001				1μ F
C4011 4015	V40120012	(16RE330)	16WV		10E
C4011, 4015	K40129012	(16RE47)	10 M A		$10\mu F$
C4008, 4014	K40109002	(10KE47)	10WV		47μF
C+000, 4014	N 40103002	(10RE47)	1044.4		+/μI'
		(10KE4/)			
		CONNECTOR			
14001	D0000202	CONNECTOR			
J4001	P0090202	B13BT-XH			

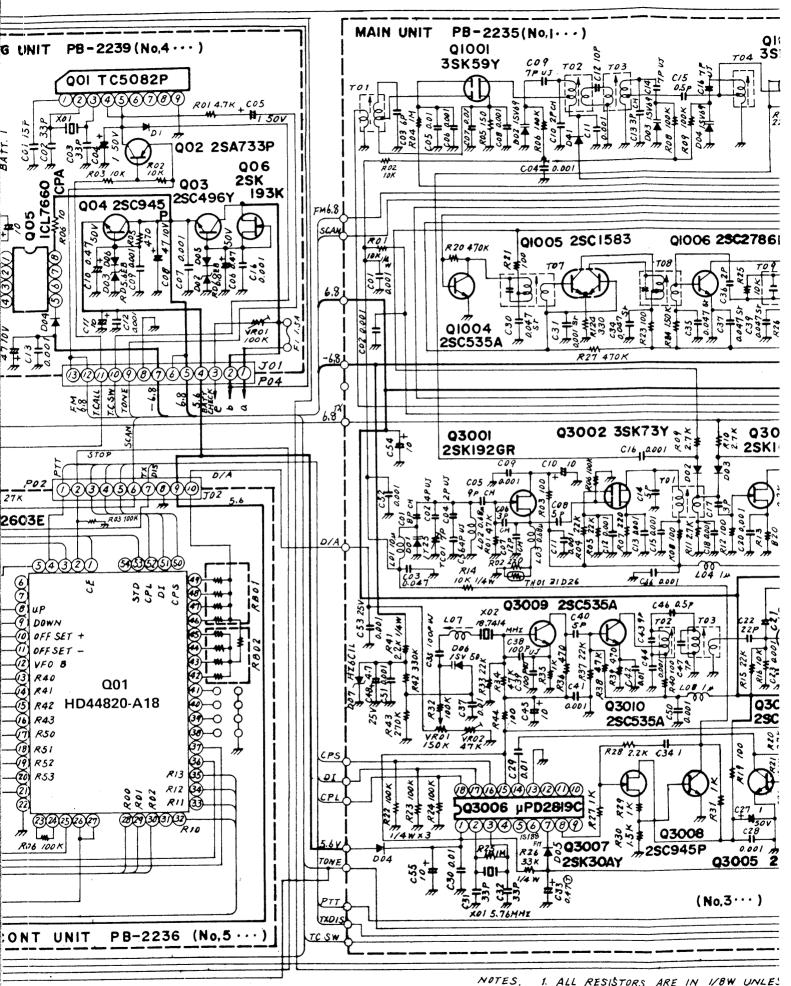
	BACKUP	BATTERY			
BAT4001	Q9000106	CR2025 3V 35m/	A h		
		FUSE			
F4001	Q0000021	L-20 1.5A			
	P2000020	FUSE HOLDER			
FH4002, 4003	P2000020	UF-0033#01			
		CONTROL UNIT			
Symbol No.	Part No.	Descripti	on		
PB-2236C	F0002236C	Printed Circuit Board	17. 1.1	<u> </u>	
	C022360A	P C B with Components	Model		
	C022360B	, 11	Model		
	C022360C	"	Model Model		
	C022360D	"	Model		
	C022360E C022360F	"	Model		
	C0223001	IC	Wiodei		
Q5001	G1090417	HD44820-A62			
Q5001 Q5002, 5004	G1090417	MC14069UB			
Q3002, 3001	010,010				
		TRANSISTOR			
Q5003	G3326030E	2SC2603E			
		DIODE			
D5002-5005,5007	G2090027	Si	1SS53		
D5006	G2090118	Schottky Barrier	1SS97		
		5.50,070.5			
	100015071	RESISTOR	1 /OW/	VJ	
R5001	J00215271	Carbon Film	1/8W	- V J	$\frac{27032}{1k\Omega}$
D.C.0.1.0	J00215102 J00215392	" "		• • • • • • • • • • • • • • • • • • • •	$3.9k\Omega$
R5019	J00215392 J00215562	,, ,,		-,,	5.6kΩ
R5008	J00215302 J00215103	,, ,,		• • • • • • • • • • • • • • • • • • • •	10kΩ
R5005 R5002	J00215103 J00215273	" "		,,	27kΩ
R5002 R5017	J00215273	" "	,,	•	47kΩ
R5017 R5003, 5006,	J00215104	" "			100kΩ
5018	300213104				
R5011, 5012,	J00215334	,, ,,	,,		330kΩ
.5014, 5015					
R5013, 5016	J00215684	" "	"	"	680kΩ

R5004, 5009, 5010	J00215105	Carbon Film	1/8W	VJ	1ΜΩ
R5007	J00215223	" "	"	"	22kΩ
		BLOCK RESISTOR			
RB5001	J40900023				···
RB5002	J40900022				
		THERMISTOR			
TH5001	G9090016	33D-28			
		CAPACITOR			
C5001	K10176391	Ceramic Disc	50WV		390pF
20001	1101/03/1	(DD104B391K 50V)	J () 17 V		230hr.
C5002, 5005,	K12171102	" "	• • • • • • • • • • • • • • • • • • • •		0.001µF
5010-5012		(DD104E102P50V)			0.001
C5006, 5008,	K19149009	Semiconductor Ceramic			0.0047μF
5009		(UAT05X472K-L05AE)			
C5007	K19149013	" "	"		0.01µF
		(UAT05X103K-L05AE)			,
C5003, 5004	K40129012	Electrolytic (ECE-A1CK100)	16WV		10μF
15001	D0000011	CONNECTOR			
J5001	P0090211	S-12B-XH 12P			
J5002	P0090211	S-10B-XH 10P			
J5003	P1090232	3024-18CH 18P			
		SWITCH			
S5001	N6090008	SSS012148			
		BUZZER			
BZ5001	M4290001	EFBRE-25D02			
		DISPLAY UNIT			
Symbol No.	Part No.	Descripti	on		
PB-2237A	F0002237A	Printed Circuit Board			
	C022370A	P C B with Components	Model A	,B,C,	D,E,F

		IC	
Q6001	G1090346	TP0401	
		LCD	
DS6001	G6090025	H1313A	
77.6004	01000046	LAMP	12V 40mA
PL6001	Q1000046	BQ031-30103A	12V 40mA
	ŀ	CEYPAD UNIT	
Symbol No.	Part No.	Descripti	on
PB-2238	F0002238	Printed Circuit Board	
	C022380A	P C B with Components	Model A,B,C,D,E
	C022380B		Model F
		DIODE	
D7003-7007	G2090027	Si	1SS53
D7003=7007	G2090027	LED	TLG205
D7002	G2090137	LED	TLR205
<i>B</i> 7 0 0 2		· · · · · · · · · · · · · · · · · · ·	
		RESISTOR	
R7001	J01215821	Carbon Film	1/8W TJ 820Ω
		SWITCH	
S7001-7007	N5090003	KEF-10901	
\$7001=7007 \$7008	N4090042	SUT 111	
37000	114070012	501111	
<u> </u>			
		CONNECTOR	
J7001	P0090210	S9B-XH	
P7001	P0090242	3022-18A	
		SWITCH HAUT (P)	
C11-81-		SWITCH UNIT (B) Descript	ion
Symbol No.	Part No.	Printed Circuit Board	1011
PB-2241A	F0002241A C022410A	P C B with Components	Model A
		r C B with Components	
	C022410B	"	Model B, C, D, E Model F
	C022410C	L	Moner

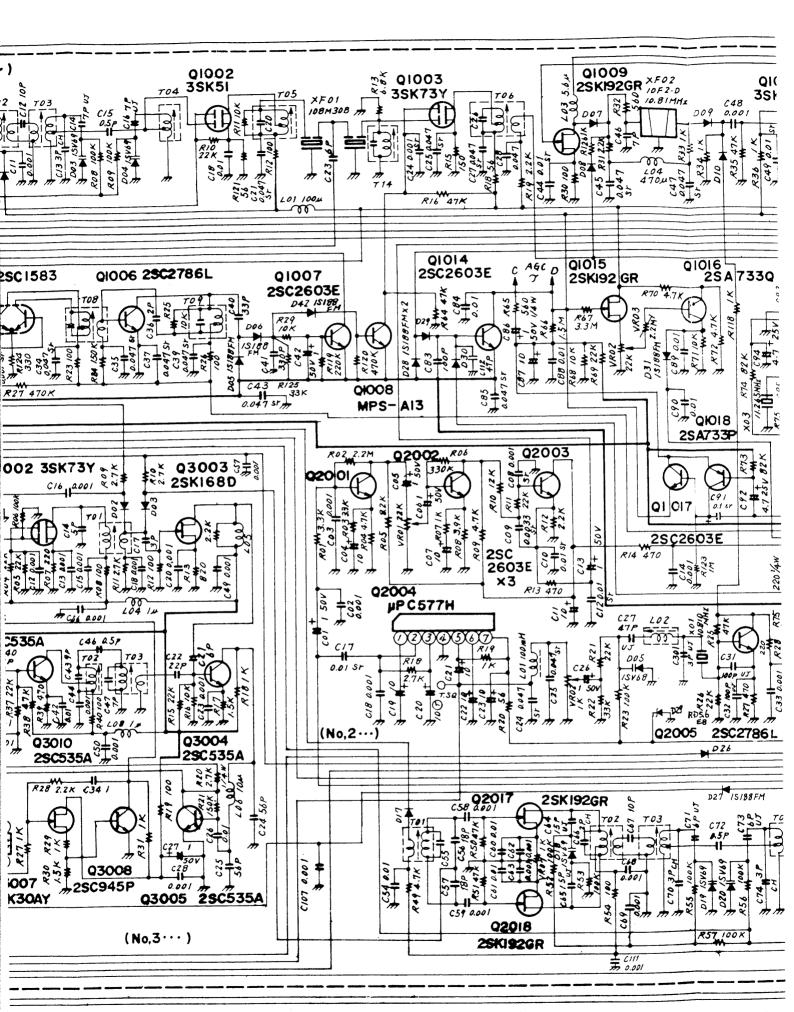
J00215223 J01215333 J00215473	2SC2603E RESISTOR Carbon Film	1/03		
J01215333	Carbon Film	1/03		
J01215333	Carbon Film	1 /037		
J01215333	Carbon Film	1 /011		
J01215333		$1/8W_{\odot}$	VJ	22kΩ
	1	"	TJ	33kΩ
JUU21JT/J	" "	,,,		47kΩ
J01215474	" "	"	TJ	470kΩ
	CAPACITOR			
K12171102	Ceramic Disc	50WV 50V)		0.001µF
			·····	
	SWITCH			
N6090007	SSS013035			
N6090008	SSS012148			
	ACCESSORIES			
Part No.		Description		
M3090033	Microphone	YM-47		
P1090253	(Microphone	e Plug FM147P)		
R7070600B	Shoulder Belt			
R0071360		nger		
P1090139	Power Plug	P-200		
P0090034	SP Plug	C-107		
	N6090007 N6090008 Part No. M3090033 P1090253 R7070600B R0071360 P1090139	CAPACITOR Capacitor Capacitor Capacitor Capacitor Ceramic Disc (DD104E102P Ceramic Disc (DD104E102P Capacitor Capaci	CAPACITOR K12171102 Ceramic Disc 50WV	CAPACITOR K12171102 Ceramic Disc S0WV (DD104E102P50V)





OTES, 1. ALL RESISTORS ARE IN 1/8W UNLES
2. ALL CAPACITORS ARE IN MF UNLES

3. ALL DIODES ARE ISS53 UNLESS 01



RESISTORS ARE IN 1/8W UNLESS OTHERWISE NOTED.
CAPACITORS ARE IN MF UNLESS OTHERWISE NOTED.
DIODES ARE 18853 UNLESS OTHERWISE NOTED

^{4.} ALL ELECTOLYTIC CAPACITORS ARE 16WY UNLESS OTH. 5. VALUE IS NOMINAL.

